

Computer Science at Kent

Mobile Processes in Unifying Theories

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Abstract

This report presents the initial work in the development of a theory of mobile processes in *Circus*, a language for describing state-based reactive systems. The mathematical basis for the work is Hoare and He's *Unifying Theories of Programming (UTP)*, where the alphabetised relational calculus is used to provide a common framework for the semantics and refinement calculus of different programming paradigms.

As our first step, we study the denotational semantics of mobile processes in UTP. Process mobility is interpreted as the assignment or communication of higher-order variables, whose values are process constants or parameterised processes, in which the target variables update their values and the source variables lose their values. We then present a set of algebraic and refinement laws to be used for the development of mobile systems. The correctness of these laws can be ensured by the UTP semantics of mobile processes.

We illustrate our theory through a simple example that can be implemented in both a centralised and a distributed way. First, we present the π -calculus specification for both systems and demonstrate that they are observationally equivalent. Next, we show how the centralised system may be derived into the distributed one using our proposed laws.

By formalising mobile processes and studying their refinement laws in the same semantic framework of *Circus*, they can be included in *Circus*' refinement calculus to enhance *Circus* with the ability to develop networks of mobile processes.

Keywords. Mobile process, refinement, UTP, higher-order programming

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1 Introduction

Mobile processes are becoming increasingly popular in software applications; they can roam from host to host in a network, carrying their own state and program code. A common example is a Java applet that is downloaded from a server across the Internet, and then executed in a client. Such processes bring new programming problems, and so require their own theoretical foundations for the correct and rigorous development of applications.

The most well-known formal theory for mobile processes is the π -calculus [18, 20, 27]. Extended from Milner's CCS [19], the π -calculus is a process algebra that provides a conceptual framework for understanding mobility, and mathematical tools for expressing mobile systems and reasoning about their behaviours [27]. In the π -calculus, it is links that move, whose simple idea is to allow processes to exchange channel names, thus change their interconnection structures dynamically. The higher-order π -calculus (HO π in short) [26] treats mobility by process passing. In the π -calculus and the HO π , the mobile and dynamic features of mobile systems can be modelled.

The semantics of the π -calculus and the HO π are typically of an operational style, which illustrates how a program can be executed by a series of steps that change the program state. It provides an equivalence relation comparing processes using bisimulation, which is defined on the structure of the behavioural graph of the two processes, where the arrows of the graph are the transitions in which the process may participate. With the operational semantics, processes are close to implementation, thus their translation to a programming language is relatively easy. However, they cannot describe system behaviour abstractly and comprehend the inner meaning of a program. The operational semantics is difficult to be used for proof, refinement and system development. Moreover, the π -calculus cannot be used to model divergence, in that it principally adopts weak barbed congruence [27] as the behavioural equivalence for π -terms, in which the internal τ action of a system is ignored.

We propose a theory of mobile processes that is suitable for refinement, so that it can support stepwise development of mobile systems. Especially, starting with an abstract specification, we develop a distributed system by using mobile processes, in which each step of the derivation is provable in the theory. A denotational understanding of mobility is essential for this purpose. Based on the denotational semantics, a refinement ordering relation, which holds between an implementation and a specification, can be defined. A crucial result in this is that we are able to deal with the specification and the implementation in the same mathematical framework.

There are also some researches in which the denotational settings of the π -calculus and HO π are studied [11, 6, 30]. These denotational approaches all involve a quite complicated type theory, domain theory or category theory, and no refinement relation based on these semantics is studied.

In this report, we present our initial results, which are denotational semantics and a set of algebraic laws for mobile processes. The mathematical basis for the work is Hoare and He's *Unifying Theories of Programming (UTP)* [13], which

uses a simple alphabetised form of Tarski's relational calculus. The correctness of the laws can be guaranteed using the UTP semantics for mobile processes. We study a simple example in both π -calculus and our theory. The results show the suitability of our laws for the stepwise development of a distributed system from a centralised specification, rather than comparing processes by using bisimulation in the π -calculus.

Process mobility is exhibited in the higher-order variable assignment or communication, in which both the source and the target are variables, and have process behaviours as their values. When a higher-order mobile-variable assignment or communication takes place, the target variable is updated, more or less as one would expect; but at the same time, the source variable becomes undefined. In this way, processes are moved around in the system.

The presence of process-valued variables has an impact on monotonicity with respect to refinement, and we require that process assignment should be monotonic in the assigned value. This follows the treatment of higher-order programming in [13].

The remainder of this report is organised as follows. The next section gives a brief overview of unifying theories and explains the reasons why we select it as the theoretical basis. Section 3 gives a simple example implemented in both a centralised and a distributed way, and shows that they are observationally equivalent using the π -calculus. Thereafter, we present the syntax, the UTP semantics, and a set of laws for mobile processes in Section 4, Section 5 and Section 6 respectively. In Section 7, we illustrate our development method of distributed system through some refinement laws; thereafter, we apply the laws in the example in Section 8. We conclude the presented work in Section 9 and outline some future work.

2 The Unifying Theories of Programming

Hoare and He's Unifying Theories of Programming (UTP) [13] is a mathematical theory for programming in which the alphabetised relation calculus and the predicate calculus are adopted as the fundamental basis for formalising and linking various theories of programming across three dimensions: different computational paradigms ([13] section 0.1), different levels of abstraction ([13] section 0.2) and distinct mathematical representations ([13] section 0.3). For each programming paradigm, programs, designs and specifications are all interpreted as relations between an initial observation and a single subsequent (intermediate or final) observation of the behaviour of the execution of a program. Program correctness and refinement can be represented by inclusion of relations. All the laws of relational calculus are valid for reasoning about correctness in all theories and in all languages.

Within the framework of the general theory of relations, formal theories differentiate one from another by their alphabet, signature, and healthiness conditions.

The *alphabets* ([13] section 0.4) of a theory are just a set of variables to record

a range of external observations of program behaviour that are considered relevant. The variable of an initial observation before the program is started is undecorated, but the variable to record the similar intermediate or final observation taken subsequently is decorated with a dash. For instance, variable x may stand for the initial value of a global variable updated by the programs, and x' denotes the final value of that variable in an intermediate state or on termination. Except the global program variables, there are some other variables globally shared with the environment in which the program involves. The first example of these external variables is the boolean variable ok whose value *true* means that the system has started in a stable state. ok' takes the value *true* when the system has reached a stable and therefore observable state; this permits a description of programs that fail due to nonterminating loops or recursion. In a theory of reactive processes, tr records the cumulative interactions between a process and its environment; and the boolean variable $wait$ distinguishes the intermediate observations of waiting states from final observations of termination. During a *wait*, the process can refuse to take part in certain events offered by the environment, which are specified by the variable ref .

The *signatures* ([13] section 0.5) of a theory are a set of operators and atomic components which provide syntax for denoting the objects of the theory. It gives the way to represent the elements of the theory by taking primitives directly as elements and using operations to construct them into larger expressions and programs. The signature of a programming theory at the lowest level of abstraction must obviously include all the notations and operations for the target programming language. Design successively removes unimplementable operators; all operators are monotonic. In the programming language, only implementable operations are left.

The *healthiness conditions* ([13] section 0.6) help designers select the required elements for a sub-theory from those of a more expressive theory in which it is embedded. By a suitable restriction of signature, design languages satisfy many healthiness conditions, and eventual target programming languages satisfy more. Thus in this elegant framework, in a top-down design process, programs form a subset of intermediate designs, and designs form a subset of specifications.

UTP has been successfully used as the theoretical foundations of the semantics and refinement calculi of various models in different areas of computing. Its power to construct a formal semantics and system development by design refinement are expressed thoroughly in these applications. In [8, 10], He, Liu and Li presented an observation-oriented semantics for an object-oriented language with a rich variety of features, including subtype, visibility, inheritance, dynamic binding, mutual recursive methods and recursions. In this model both class declarations and commands are identified as UTP designs. UTP relational model is also used for specifying and reasoning in activities of an object system development process in [9] where a refinement calculus for object systems is proposed. Li and He [15] provided a denotational semantics to a subset of Time RAISE [7] Specification Language (TRSL) using Duration Calculus [37] model, borrowing a lot of ideas from UTP. Based on this work, recently Ri and He [23] presented a technique for verifying the correctness of TRSL specification against

the real-time requirements described as predicates over the observables; a set of proof rules are developed and their soundness and completeness is proved. This work follows again the idea and methodology of UTP. In [32] Woodcock developed a formal semantics and refinement calculus for a shared-variable parallel programming language using the concepts and notations of UTP. In [33] Woodcock and Cavalcanti used UTP to formally define the semantics of a concurrent refinement language *Circus* that integrates CSP [12, 24], Z [36], specification statements [21] and guarded commands [5], for describing state-based reactive systems. With the same UTP semantics model, Sherif and He naturally extended *Circus* with discrete time in [29] and proposed in [28] a framework for the specification, validation and development of real time program using *Circus*. In *Circus* and timed *Circus*, UTP is demonstrated to be a powerful theory to unify different models. Similar work was done later by Qin, Dong and Chin in [22] by constructing a UTP semantics model for another integrated language — Timed Communicating Object Z (TCOZ) [17]. More recently, Jin and He proposed a hierarchy of four semantics models for specifying and reasoning programs with limited resources [14] following the design methodology in UTP. Each model in the hierarchy is a refinement of the one defined formerly.

As a powerful theoretical foundation for the denotational semantics and refinement calculi of so many different models, we believe UTP can also be used for our formalisms and development method for mobile processes. The further reasons why we select it as the theoretical basis are:

- UTP serves as the mathematical base of *Circus* [34]. We intend to include the refinement of mobile processes in the refinement calculus of *Circus*. It is adequate to use UTP — the same semantics framework of *Circus* — to study mobile processes, otherwise this inclusion will be very difficult or impossible.
- The approaches to communication and higher-order programming in UTP ([13] chapter 8 and 9) are applicable to mobile processes. Communication is one of the important features of mobile processes. Moreover, as processes themselves can be communicated through channels, we need to formalise mobile processes in higher-order programming.

3 A Simple Example in the π -Calculus

Suppose that there is a data centre that needs to analyse data based on information residing in different hosts on a network. For simplicity, we assume that this analysis amounts to getting the sum of the data in each host.

This small application can be implemented in two ways. In the first implementation (Figure 1), the data center directly communicates with each host, one by one, getting the local information and then updating the data. All the calculations for update are carried out in the data centre. This implementation is very simple, and so is obviously correct.

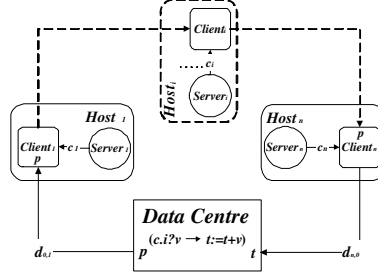
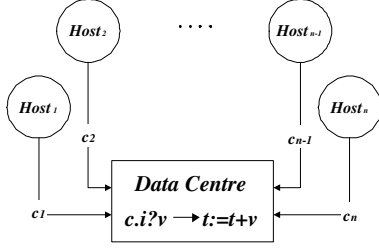


Figure 1: Centralised Implementation Figure 2: Distributed Implementation

In the second implementation (Figure 2), similar pieces of specification are abstracted and encapsulated in a parameterised process variable, which travels from the data centre, and roams the network to each host, taking its local state, and the operations used to update the local state. After its arrival at a host, it is plugged into a local channel c_i and activated, getting the local information and updating the local state. After visiting all hosts, it comes back to the data centre with the final result.

Both implementations can complete the simple task, but the latter one has a richer structure that might make it more useful. For example, if the nature of the analysis changes, then only the mobile process needs to be changed. Consider an international bank: it may change both data (exchange rates) and function (services offered to customers) frequently. If this is programmed as a mobile process, then the very latest upgrade arrives automatically in local branches. It may also be more efficient to move the process from the centre to each host, as local communication would replace remote communication.

Both systems can be specified in the π -calculus. As the π -calculus syntax does not have assignment, we use a suitable translation to convert assignment into π -terms as the composition of an input and an output action over a restricted (or bound) name. The π -calculus syntax that we adopt is from [20].

Definition 3.1 (π -calculus assignment)

$$\llbracket (t := e).P \rrbracket \hat{=} \text{new } h (\bar{h}\langle e \rangle.0 \mid h(t).\llbracket P \rrbracket) \quad \{t \in \text{fn}(P), h \notin \text{fn}(P)\}$$

where the part enclosed in $\llbracket \cdot \rrbracket$ is the side condition that the definition should satisfy, and $\text{fn}(P)$ denotes the free names of P . \square

Observationally, the effect of the assignment of a value to variable t followed by executing P immediately is the same as substituting this value for all the free occurrences of the variable t in P . This is shown by the following simple theorem that states the bisimilarity between the two.

Theorem 3.1 (Assignment bisimulation)

$$(t := e).P \approx \{e/t\}P \quad \{t \in \text{fn}(P)\}$$

where $\{e/t\}P$ denotes the systematic substitution of e for t in P .

Proof:

$$\begin{aligned}
& LHS && \{\text{definition of assignment}\} \\
& = \text{new } h \ (\bar{h}\langle e \rangle.0 \mid h(t).P) && \{\text{strong bisimilarity}\} \\
& \sim \tau.(\mathbf{0} \mid \{e/t\}P) && \{\text{weak bisimilarity}\} \\
& \approx \mathbf{0} \mid \{e/t\}P && \{\text{structural congruence}\} \\
& \equiv RHS
\end{aligned}$$

The centralised system is the composition of a *Centre* process and n *Hosts* over restricted channel names.

$$\text{System} \hat{=} (\text{new } c_1, c_2, \dots, c_n)(\text{Centre} \mid \text{Host}_1 \mid \text{Host}_2 \mid \dots \mid \text{Host}_n)$$

where the *Centre* and *Host_i* are defined as:

$$\begin{aligned}
\text{Centre} & \hat{=} (t := 0).c_1(v).(t := t + v).\dots.c_n(v).(t := t + v).\text{result}\langle t \rangle \\
\text{Host}_i & \hat{=} \bar{c}_i\langle u_i \rangle
\end{aligned}$$

where $c_i(v)$ is the input of the data that needs to be retrieved from the i th host, and u_i is the data that needs to be retrieved from the i th host. We sometimes abbreviate $\text{result}\langle t \rangle.0$ as $\text{result}\langle t \rangle$.

In the assignment $t := t + v$ of *Centre*, the t in the left hand side is a restricted name while the t in the right hand side is a free name. We can better comprehend the scope of each t and v using renaming. The effect of *Centre* is the same as

$$\begin{aligned}
\text{Centre} & \hat{=} \\
& (t_0 := 0).c_1(v_1).(t_1 := t_0 + v_1).\dots.c_n(v_n).(t_n := t_{n-1} + v_n).\text{result}\langle t_n \rangle
\end{aligned}$$

Observationally, *System* is equivalent to $\text{result}\langle u_1 + u_2 + \dots + u_n \rangle$.

Theorem 3.2 (Centralised system bisimulation)

$$\text{System} \approx \text{result}\langle u_1 + u_2 + \dots + u_n \rangle$$

Proof:

$$\begin{aligned}
& LHS && \{\text{definition}\} \\
& = (\text{new } c_1, c_2, \dots, c_n) \\
& \quad \left(\begin{array}{l} ((t := 0).c_1(v_1).(t_1 := t + v_1).\dots.c_n(v_n).(t_n := t_{n-1} + v_n).\text{result}\langle t_n \rangle) \\ | \bar{c}_1\langle u_1 \rangle \mid \bar{c}_2\langle u_2 \rangle \mid \dots \mid \bar{c}_n\langle u_n \rangle \end{array} \right) \\
& && \{\text{structural congruence}\} \\
& \equiv (t := 0).(\text{new } c_1)(c_1(v_1) \mid \bar{c}_1\langle u_1 \rangle).(t_1 := t + v_1).\dots \\
& \quad (\text{new } c_n)(c_n(v_n) \mid \bar{c}_n\langle u_n \rangle).(t_n := t_{n-1} + v_n).\text{result}\langle t_n \rangle \quad \{\text{strong bisimilarity}\} \\
& \sim^* (t := 0).\tau.(t_1 := t + u_1).\dots \\
& \quad (\text{new } c_n)(c_n(v_n) \mid \bar{c}_n\langle u_n \rangle).(t_n := t_{n-1} + v_n).\text{result}\langle t_n \rangle
\end{aligned}$$

$$\begin{aligned}
& \{\text{definition of } t := 0, \text{ Theorem 3.1, strong bisimilarity}\} \\
& \sim \tau.\tau.(t_1 := u_1).\tau.(t_2 := t_1 + u_2).\dots \\
& \quad (\text{new } c_n)(c_n(v_n) \mid \overline{c_n}\langle u_n \rangle).(t_n := t_{n-1} + v_n).\text{result}\langle t_n \rangle \quad \{\text{weak bisimilarity}\} \\
& \approx t_1 := u_1.\tau.t_2 := t_1 + u_2.\dots \\
& \quad (\text{new } c_n)(c_n(v_n) \mid \overline{c_n}\langle u_n \rangle).(t_n := t_{n-1} + v_n).\text{result}\langle t_n \rangle \\
& \qquad \qquad \qquad \{\text{induction over the indices as from step}^*\} \\
& \approx \tau.\text{result}\langle u_1 + u_2 + \dots + u_n \rangle \\
& \approx \text{RHS} \qquad \qquad \qquad \{\text{weak bisimilarity}\}
\end{aligned}$$

□

In the HO π [26] and polyadic π -calculus [20], abstractions (parameterised processes) or multiple names can be transmitted directly along channel names. In the distributed implementation, an abstraction $(z, in, out, dn).P$ and the value of t are transmitted at the same time, where $(z, in, out, dn).P$ and its execution $((z, in, out, dn).P)\langle c_i, t_{i-1}, t_i, tg \rangle$ are defined as follows:

$$\begin{aligned}
(z, in, out, dn).P & \hat{=} z(v).(\text{out} := in + v).\overline{dn}\langle out \rangle.0 \\
((z, in, out, dn).P)\langle c_i, t_{i-1}, tmp, tg \rangle & \hat{=} c_i(v).(\text{tmp} := t_{i-1} + v).\overline{tg}\langle tmp \rangle.0
\end{aligned}$$

Let $d_{0,1}$ be the name of the channel connecting *Centre* and *Host*₁, $d_{n,0}$ be the name of the channel connecting *Host*_n and *Centre*, and $d_{i,i+1}$ ($1 \leq i \leq n-1$) be the name of the channel connecting *Host*_i and *Host*_{i+1}. The specification for the distributed system is

$$\begin{aligned}
MSystem & \hat{=} \\
& (\text{new } d_{0,1}, d_{1,2}, \dots, d_{n,0})(M\text{Centre} \mid M\text{Host}_1 \mid M\text{Host}_2 \mid \dots \mid M\text{Host}_n)
\end{aligned}$$

The *Centre* now has the task of initialising its total, sending the mobile process and total on its way, and then waiting for the total to return home¹, before outputting the result.

$$M\text{Centre} \hat{=} (t := 0).\overline{d_{0,1}}\langle (z, in, out, dn).P, t \rangle.d_{n,0}(\text{final}).\text{result}\langle \text{final} \rangle.0$$

Each host now has an extra component: one that receives the mobile process, executes it, and then passes it on to the next host. This component is merely added to the previous behaviour of the host.

$$\begin{aligned}
M\text{Host}_i & \hat{=} (\text{new } c_i)(\text{Host}C_i \mid \text{Host}_i) \\
\text{Host}C_i & \hat{=} \\
& \begin{cases} d_{i-1,i}(p, t_{i-1}).(\text{new } tg)(p\langle c_i, t_{i-1}, tmp, tg \rangle \mid tg(t_i).\overline{d_{i,i+1}}\langle p, t_i \rangle.0) \\ \qquad \qquad \qquad \text{for } i = 1 \dots n-1 \\ d_{n-1,n}(p, t_{n-1}).(\text{new } tg)(p\langle c_i, t_{n-1}, tmp, tg \rangle \mid tg(t_n).\overline{d_{n,0}}\langle t_n \rangle.0) \\ \qquad \qquad \qquad \text{for } i = n \end{cases}
\end{aligned}$$

¹The mobile process is discarded in the last-visited host after its mission in order to save network cost, but extra cost arises for specifying the last host separately.

In $HostC_i$, we use a restricted name tg to make the output via $d_{i,i+1}$ possible after p 's execution. If we discard $\overline{dn}\langle out \rangle$ in the transmitted abstraction, then the value of the updated t would not be passed on, and $p\langle c_i, t_{i-1}, tmp \rangle.\overline{d_{i,i+1}}\langle p, t_i \rangle.0$ would be syntactically wrong, because $p\langle c_i, t_{i-1}, tmp \rangle$ is a process rather than a prefix.

By using some rules in $HO\pi$ and polyadic π -calculus, we can get an equivalence to $MSystem$: it is also weakly bisimilar to $result\langle u_1 + u_2 + \dots + u_n \rangle$.

Theorem 3.3 (Distributed system bisimulation)

$$MSystem \approx result\langle u_1 + u_2 + \dots + u_n \rangle$$

Proof: Let $A_i = (new\ tg)(p\langle c_i, t_{i-1}, tmp, tg \rangle \mid tg(t_i).\overline{d_{i,i+1}}\langle p, t_i \rangle.0)$

$$MSystem \quad \{\text{def of } M\text{Centre and } M\text{Host}\}$$

$$\equiv^* (new\ d_{0,1}, d_{1,2}, \dots, d_{n,0})$$

$$\left(\begin{array}{l} t := 0.\overline{d_{0,1}}\langle P, t \rangle.d_{n,0}(final).result\langle final \rangle \\ | (new\ c_1)(d_{0,1}(p, t_0).A_1 \mid \overline{c_1}\langle u_1 \rangle) \\ | (new\ c_2)(d_{1,2}(p, t_1).A_2 \mid \overline{c_2}\langle u_2 \rangle) \\ | \dots \\ | (new\ c_{n-1})(d_{n-2,n-1}(p, t_{n-2}).A_{n-1} \mid \overline{c_{n-1}}\langle u_{n-1} \rangle) \\ | (new\ c_n) \\ | (d_{n-1,n}(p, t_{n-1}).(new\ tg)(p\langle c_n, t_{n-1}, tmp, tg \rangle \mid tg(t_n).\overline{d_{n,0}}\langle t_n \rangle) \mid \overline{c_n}\langle u_n \rangle) \end{array} \right)$$

{structural congruence, def of $t := 0$ }

$$\sim (new\ d_{1,2}, \dots, d_{n,0})$$

$$\left(\begin{array}{l} \left(\begin{array}{l} \tau.(new\ d_{0,1}) \left(\begin{array}{l} \overline{d_{0,1}}\langle P, 0 \rangle \\ | (new\ c_1)(d_{0,1}(p, t_0).A_1 \mid \overline{c_1}\langle u_1 \rangle) \end{array} \right) \\ .d_{n,0}(final).result\langle final \rangle \end{array} \right) \\ | (new\ c_2)(d_{1,2}(p, t_1).A_2 \mid \overline{c_2}\langle u_2 \rangle) \\ | \dots \\ | (new\ c_{n-1})(d_{n-2,n-1}(p, t_{n-2}).A_{n-1} \mid \overline{c_{n-1}}\langle u_{n-1} \rangle) \\ | (new\ c_n) \\ | (d_{n-1,n}(p, t_{n-1}).(new\ tg)(p\langle c_n, t_{n-1}, tmp, tg \rangle \mid tg(t_n).\overline{d_{n,0}}\langle t_n \rangle) \mid \overline{c_n}\langle u_n \rangle) \end{array} \right)$$

{synchronisation over $d_{0,1}$, def of $p\langle c_1, t_0, tmp, tg \rangle$ }

$$\sim (new\ d_{1,2}, \dots, d_{n,0})$$

$$\left(\begin{array}{l} \left(\begin{array}{l} \tau.\tau.(new\ c_1) \\ ((new\ tg)(c_1(v).tmp := 0 + v.\overline{tg}\langle tmp \rangle \mid tg(t_1).\overline{d_{1,2}}\langle P, t_1 \rangle) \mid \overline{c_1}\langle u_1 \rangle) \\ .d_{n,0}(final).result\langle final \rangle \end{array} \right) \\ | (new\ c_2)(d_{1,2}(p, t_1).A_2 \mid \overline{c_2}\langle u_2 \rangle) \\ | \dots \\ | (new\ c_{n-1})(d_{n-2,n-1}(p, t_{n-2}).A_{n-1} \mid \overline{c_{n-1}}\langle u_{n-1} \rangle) \\ | (new\ c_n) \\ | (d_{n-1,n}(p, t_{n-1}).(new\ tg)(p\langle c_n, t_{n-1}, tmp, tg \rangle \mid tg(t_n).\overline{d_{n,0}}\langle t_n \rangle) \mid \overline{c_n}\langle u_n \rangle) \end{array} \right)$$

{synchronisation over tg , def of $tmp := v$ }

$$\sim (new\ d_{1,2}, \dots, d_{n,0})$$

$$\begin{aligned}
& \left(\begin{array}{l} \left(\begin{array}{l} \tau.\tau.(\text{new } c_1)(c_1(v).\tau.\tau.\overline{d_{1,2}}\langle P, v \rangle) \mid \overline{c_1}\langle u_1 \rangle \\ .d_{n,0}(\text{final}).\text{result}\langle \text{final} \rangle \end{array} \right) \\ \mid (\text{new } c_2)(d_{1,2}(p, t_1).A_2 \mid \overline{c_2}\langle u_2 \rangle) \\ \mid \dots \\ \mid (\text{new } c_{n-1})(d_{n-2, n-1}(p, t_{n-2}).A_{n-1} \mid \overline{c_{n-1}}\langle u_{n-1} \rangle) \\ \mid (\text{new } c_n) \\ \mid (d_{n-1, n}(p, t_{n-1}).(\text{new } tg)(p\langle c_n, t_{n-1}, tmp, tg \rangle \mid tg(t_n).\overline{d_{n,0}}\langle t_n \rangle) \mid \overline{c_n}\langle u_n \rangle) \end{array} \right) \\
& \hspace{15em} \{\text{synchronisation over } c_1\} \\
& \sim (\text{new } d_{1,2}, \dots, d_{n,0}) \\
& \left(\begin{array}{l} \left(\begin{array}{l} \tau.\tau.\tau.(\tau.\tau.\overline{d_{1,2}}\langle P, u_1 \rangle) \\ .d_{n,0}(\text{final}).\text{result}\langle \text{final} \rangle \end{array} \right) \\ \mid (\text{new } c_2)(d_{1,2}(p, t_1).A_2 \mid \overline{c_2}\langle u_2 \rangle) \\ \mid \dots \\ \mid (\text{new } c_{n-1})(d_{n-2, n-1}(p, t_{n-2}).A_{n-1} \mid \overline{c_{n-1}}\langle u_{n-1} \rangle) \\ \mid (\text{new } c_n) \\ \mid (d_{n-1, n}(p, t_{n-1}).(\text{new } tg)(p\langle c_n, t_{n-1}, tmp, tg \rangle \mid tg(t_n).\overline{d_{n,0}}\langle t_n \rangle) \mid \overline{c_n}\langle u_n \rangle) \end{array} \right) \\
& \hspace{15em} \{\text{weak bisimilarity}\} \\
& \approx (\text{new } d_{1,2}, \dots, d_{n,0}) \\
& \left(\begin{array}{l} \overline{d_{1,2}}\langle P, u_1 \rangle.d_{n,0}(\text{final}).\text{result}\langle \text{final} \rangle \\ \mid (\text{new } c_2)(d_{1,2}(p, t_1).A_2 \mid \overline{c_2}\langle u_2 \rangle) \\ \mid \dots \\ \mid (\text{new } c_{n-1})(d_{n-2, n-1}(p, t_{n-2}).A_{n-1} \mid \overline{c_{n-1}}\langle u_{n-1} \rangle) \\ \mid (\text{new } c_n) \\ \mid (d_{n-1, n}(p, t_{n-1}).(\text{new } tg)(p\langle c_n, t_{n-1}, tmp, tg \rangle \mid tg(t_n).\overline{d_{n,0}}\langle t_n \rangle) \mid \overline{c_n}\langle u_n \rangle) \end{array} \right) \\
& \hspace{15em} \{\text{induction over the indices as from step}^*\} \\
& \approx \text{result}\langle u_1 + u_2 + \dots + u_n \rangle
\end{aligned}$$

□

Theorem 3.2 and Theorem 3.3 show that the π -calculus can be used for verification. If we have got a specification in the π -calculus, it is possible for us to use the structural congruence rules and strong/weak bisimulation rules to deduce that this specification satisfies some requirement. Both *System* and *MSystem* are observationally equivalent to $\text{result}\langle u_1 + u_2 + \dots + u_n \rangle$, so they both satisfy the requirement of getting the sum of the data in each host.

We are interested in a step-wise development process in the spirit of Morgan's refinement calculus for sequential programs [21]. We would like to start from an abstract, centralised specification, and develop a concrete, distributed implementation, proceeding in small steps that are easy to justify and that explain design decisions. It is not easy to follow this discipline in the π -calculus. Instead, we would like to base our language of mobile processes firmly on the notion of refinement, and develop sets of laws that encourage piece-wise development. In later sections, after we present the denotational semantics and refinement laws for mobile processes, we show this step-wise development in our proposed approach.

4 Syntax

The syntax of our language is a subset of *occam* [16] and CSP [12, 24], but enriched with primitives for process variables, mobile and clone assignment and communication, and (parameterised) process variable activation. These mobility constructs are inspired by *occam_M* [1].

In discussing the semantics, we make use of the following conventions for meta-variables. p and q range over all program variables; t ranges over data variables; e ranges over data; h ranges over process variables; E ranges over data or process values; x , y and z range over formal name, value, and result parameters; ne , ve , and re range over actual name, value, and result parameters; b ranges over boolean values; X ranges over sets of events.

The basic elements in our model are processes, which are constructed from the following rules:

$$\begin{aligned}
P & ::= \textit{SKIP} \mid \textit{STOP} \mid \textit{CHAOS} \mid \mathbf{vid} \ p : T \mid \mathbf{end} \ p \\
& \quad \mid h := \{P\} \mid h := \{\lambda x : \mathit{var}(T_1), y : \mathit{val}(T_2), z : \mathit{res}(T_3) \bullet P\} \\
& \quad \mid t := e \mid p :=_m q \mid p := q \mid \langle h \rangle \mid h(ne, ve, re) \\
& \quad \mid \textit{Comm} \rightarrow P \mid P \triangleleft b \triangleright Q \mid P ; Q \mid b * P \\
& \quad \mid P \parallel Q \mid P \square Q \mid P \sqcap Q \mid P \setminus X \\
\mathbf{vid} & ::= \mathbf{var} \mid \mathbf{proc} \\
\textit{Comm} & ::= \textit{ch?}p \mid \textit{ch!}q \mid \textit{ch!!}q \mid \textit{ch.E}
\end{aligned}$$

SKIP does nothing and terminates immediately; *STOP* represents deadlock; *CHAOS* is the worst process and the bottom element of the complete lattice of mobile process: its behaviour is arbitrary.

The variable declaration $\mathbf{vid} \ p : T$ introduces a new variable p of type T ; correspondingly $\mathbf{end} \ p$ terminates the scope of p . When it is clear from the context that p is a data variable or a process variable, we use \mathbf{var} or \mathbf{proc} for its declaration. The type T determines the range of values that p can have. When p is a process variable, its type determines the alphabet and interface² of the process that may be assigned to p . For convenience, we often omit types when they are irrelevant.

Higher-order programming treats program as data, and higher-order assignment or communication assigns process constants to higher-order variables. Process constants are enclosed in brackets, which have no semantic importance and are ignored when the process is activated. We distinguish simple process constants from parameterised ones. The higher-order constant assignment $h := \{P\}$ assigns a simple process constant P to h , and $h := \{\lambda x : \mathit{var}(T_1), y : \mathit{val}(T_2), z : \mathit{res}(T_3) \bullet P\}$ assigns h a parameterised process constant, which has a body P and a formal name parameter x , value parameter y , and result parameter z .

The first-order constant assignment $t := e$ assigns a value e to the data variable t . The clone variable assignment $p := q$ is similar to constant assignment, except that the term in its right hand side is a variable rather than a

²The interface is defined as parameters and input/output channels through which the process can interact with its environment.

value. After this clone assignment, the value of p is updated according to q 's value, and q gets a value that is better than its original one (in Section 5.3, we explain what a better value is and why the value should be better). The notation $p :=_m q$ denotes mobile variable assignment. On its termination, the value of the target variable p is updated and the source variable q is undefined, thus the result of any attempt of using q is unpredictable.

The notation $ch.E$ stands for a communication that takes place as soon as both participants are ready. By executing the input prefix $ch?p \rightarrow P$, the variable p accepts a message from the channel, and then the program behaves like P . The mobile output prefix $ch!q \rightarrow P$ transfers the value of variable q through channel ch and then executes P . As in mobile assignment, any attempt to use q after output is unpredictable. The clone output prefix $ch!q \rightarrow P$ outputs the value of q , but retains q 's value.

Once initialised, a process variable h may be activated by executing it, denoted by $\langle h \rangle$. A parameterised process variable can be activated by providing parameters.

A conditional $(P \triangleleft b \triangleright Q)$ behaves as P if b is *true*, otherwise as Q . The sequential composition of two processes $(P ; Q)$ results in a process that behaves as P , and, on termination of P , behaves as Q . An iteration $(b * P)$ repeatedly executes P until b is *false*. A parallel composition $(P \parallel Q)$ executes P and Q concurrently, such that events in the alphabet of both parties require their simultaneous participation, whereas the remaining events occur independently. An external choice $(P \square Q)$ allows the environment to choose between P and Q , whereas the internal choice $(P \sqcap Q)$ selects one of the two processes nondeterministically. $P \setminus X$ hides the events in the set X , so that they happen invisibly, without the participation of the environment.

5 Semantics

This section presents the denotational semantics for mobile processes. There are two main differences in our approach of higher-order programming with conventional first-order programming. In first-order programming, we only discuss the refinement ordering between processes. In our approach, however, as mobile processes are modelled as the values of higher-order variables, there are two new refinement orderings: refinement between variables and refinement between higher-order variables and processes. These three refinement orderings are defined in Section 5.1. After giving the UTP model that we use for the semantics of mobile processes in Section 5.2, we present the other difference which is in the semantics of higher-order assignment and communication in Section 5.3. Finally, the denotational semantics is given in the Section 5.4.

5.1 Refinement orderings

In first-order programming, we say a process P is refined by Q , denoted by $P \sqsubseteq Q$, if for all variables in the alphabet, the behaviour of Q is more predictable

and controllable than that of P .

Definition 5.1 (Process refinement)

$$P \sqsubseteq Q \hat{=} \forall \mathbf{obs}, \mathbf{obs}', v, v' \bullet Q \Rightarrow P$$

where \mathbf{obs}, v are observable and program variables (see Section 5.2.1 for details) respectively. The universal closure over the alphabet is written more concisely: $[Q \Rightarrow P]$. \square

As higher-order variables hold processes as their values, the ordering between program variables can be defined in the light of the refinement ordering between their values. Two process variables can be compared only when they have the same types. We say a process variable h is a refinement of g , if the activation behaviour of h is more controllable and predictable than that of g . For first-order data variables, two variables are comparable only when they are equal. More specifically, we define the refinement ordering between variables as follows.

Definition 5.2 (Variable refinement) *Let p and q be two program variables of the same type*

$$p \sqsubseteq q \hat{=} \begin{cases} p = q & \text{if } p, q \text{ are data variables} \\ [q(ne, ve, re) \Rightarrow p(ne, ve, re)] & \text{if } p, q \text{ are parameterised process variables} \\ [\langle q \rangle \Rightarrow \langle p \rangle] & \text{otherwise} \end{cases}$$

where ne , ve , and re are the actual name parameter, value parameter and result parameter for the activations of p and q . \square

A refinement ordering between process variable and process can be defined similarly.

Definition 5.3 (Variable process refinement) *Suppose h is a process variable and Q is a process, h and Q have the same type.*

$$h \sqsupseteq Q \hat{=} \begin{cases} [h(ne, ve, re) \Rightarrow Q(ne, ve, re)] & \text{if } h \text{ and } Q \text{ are parameterised} \\ [\langle h \rangle \Rightarrow Q] & \text{otherwise} \end{cases}$$

where \mathbf{obs}, v and ne, ve, re are same as that in Definition 5.2. \square

5.2 UTP semantics model

In this section we present the UTP semantics model to be used to formalise mobile processes.

5.2.1 Alphabet

In the UTP, a process P is described in terms of an implicit description of its entire behaviour using the alphabet. The alphabet of our model consists of the following external observable variables and program variables:

- \mathcal{A} : the set of events in which P can engage
- ok, ok' : \mathbb{B} , indicates divergence. $ok = true$ represents that P has been properly started in a stable state. $ok' = true$ indicates subsequent stabilisation in an observational state, either in an intermediate state or a final one, and $ok' = false$ indicates the program is divergent.
- $wait, wait'$: \mathbb{B} , indicates termination. $wait = true$ means that P has been started in an intermediate state, and $wait' = true$ indicates that the program has not terminated in which case all the other dashed variables stand for intermediate observations rather than final ones.
- tr, tr' : $\text{seq } \mathcal{A}$. They represent the cumulative records of all interactions that P has made so far. tr records the sequence of events which took place before P started, and tr' refers to the sequence of all events at the moment when the observation takes place.
- ref, ref' : $\mathbb{P}\mathcal{A}$. ref represents the set of events refused by P before it has started, and ref' stands for the set of events refused by P at the time of observation.
- v, v' : all program variables including process variables and data variables

We use **obs** to represent all observable variables $ok, wait, tr, ref$ in short. P 's behaviour is described by a relation between undashed and dashed variables. By using this model, the refusal can be indicated by ref , and the divergence is captured by the ok variable. The boolean $wait$ variable describes the state in which the process is waiting for interaction with the environment. Therefore, we are able to reason about the refusal and divergence behaviours of mobile processes.

We use αP to denote the alphabet of process P , and

$$\alpha P = in\alpha P \cup out\alpha P$$

where $in\alpha P$ is a set of undashed variables recording initial values, and $out\alpha P$ is a set of dashed variables recording final values.

Usually, we use subscript to indicate the alphabet of a process. For instance, P_A denotes a process P of alphabet A . The alphabet of a process can be extended with a new variable by conjunction with a predicate that indicates the final value of this variable is a refinement of its initial value. The reason why we adopt refinement rather than equivalence will be explained in Section 5.3.

Definition 5.4 (Alphabet extension) *Let $p, p' \notin \alpha P$*

$$\begin{aligned} P_{+p} &\hat{=} P \wedge (p' \sqsupseteq p) \\ \alpha(P_{+p}) &\hat{=} \alpha P \cup \{p, p'\} \end{aligned} \quad \square$$

Alphabet extension is often needed to make sure sequential composition works properly. Further discussion will be shown in a later section.

5.2.2 Healthiness conditions

Healthiness conditions distinguish feasible specifications or designs from infeasible ones, where infeasibility means impossible implementation in the target programming language. There are five healthiness conditions for mobile processes as follows.

$$\begin{aligned} \mathbf{M1} \quad P &= P \wedge (tr \leq tr') \\ \mathbf{M2} \quad P &= \sqcap \{P[s, s \hat{\wedge} (tr' - tr)/tr, tr'] \mid s \in \text{seq } \mathcal{A}\} \\ \mathbf{M3} \quad P &= II \triangleleft \text{wait} \triangleright P \\ &\text{where} \\ II &\hat{=} I \triangleleft \text{ok} \triangleright (tr \leq tr') \\ I &\hat{=} (ok' = ok) \wedge (tr' = tr) \wedge (\text{wait}' = \text{wait}) \wedge (\text{ref}' = \text{ref}) \wedge (v' \sqsupseteq v) \\ \mathbf{M4} \quad P &= P \triangleleft \text{ok} \triangleright (tr \leq tr') \\ \mathbf{M5} \quad P &= P ; J \\ &\text{where} \\ J &\hat{=} (ok \Rightarrow ok') \wedge (tr' = tr) \wedge (\text{wait}' = \text{wait}) \wedge (\text{ref}' = \text{ref}) \wedge (v' \sqsupseteq v) \end{aligned}$$

Let

$$\begin{aligned} \mathbf{M1}(P) &= P \wedge (tr \leq tr') \\ \mathbf{M2}(P) &= \sqcap \{P[s, s \hat{\wedge} (tr' - tr)/tr, tr'] \mid s \in \text{seq } \mathcal{A}\} \\ \mathbf{M3}(P) &= II \triangleleft \text{wait} \triangleright P \\ \mathbf{M4}(P) &= P \triangleleft \text{ok} \triangleright (tr \leq tr') \\ \mathbf{M5}(P) &= P ; J \end{aligned}$$

thus every healthiness condition \mathbf{Mi} can be written as

$$P = \mathbf{Mi}(P)$$

If P satisfies it, we say ' P is \mathbf{Mi} ' or ' P is \mathbf{Mi} healthy'. In later discussion, we will use abbreviations to represent function composition and healthiness conditions for easier presentation. For instance, $\mathbf{M23}(P)$ is used to refer to $\mathbf{M2} \diamond \mathbf{M3}(P)$, ' P is $\mathbf{M12}$ ' refers to P satisfies ' $\mathbf{M1}$ and $\mathbf{M2}$ '.

The sequential composition $(P ; Q)$ behaves as P until P terminates successfully, at which point it passes control to Q .

Definition 5.5 (Sequential composition) *Provided $\text{out } \alpha P = \text{in } \alpha' Q = \{\alpha'\}$, then*

$$\llbracket P ; Q \rrbracket \hat{=} \exists \alpha_0 \bullet \llbracket P \rrbracket[\alpha_0/\alpha'] \wedge \llbracket Q \rrbracket[\alpha_0/\alpha] \quad \square$$

In the sequential composition $(P ; Q)$, the final state of P that is recorded by α' is passed on as the initial state of Q that is recorded by α , but this state is an intermediate state of $(P ; Q)$, and cannot be observed from the environment. The definition uses existential quantification to hide the intermediate observation, and to remove the variables that record it from the alphabet. Thus a new set of variables α_0 is introduced to record the hidden observation.

In reactive programming, the sequential composition has three possible states. The first state is that if the first process diverges, then the sequential composition does as well. The second state is that if the first process is in a waiting state, then the following process cannot start. The third is that if the first process terminates and does not diverge, then the following process starts immediately after the first one terminates. Alternatively, the sequential composition in reactive programming is the same as

$$\begin{aligned} \llbracket P ; Q \rrbracket &= \llbracket P \rrbracket[\text{false}/ok'] \vee \llbracket P \rrbracket[\text{true}/wait'] \\ &\vee (\llbracket P \rrbracket[\text{true}, \text{false}/ok', \text{wait}'] \diamond \llbracket Q \rrbracket) \end{aligned}$$

where the operator \diamond is to denote the concatenation of two sequentially made observations. For two observation predicates $A(o'), B(o)$, where o, o' represent the initial and final value of all the observation variables respectively, the concatenation of them is defined as

$$A(o') \diamond B(o) \hat{=} \exists o_0 \bullet A(o_0) \wedge B(o_0)$$

Because the alphabet of a process can always be extended as defined in Definition 5.4, we can ensure that any sequential composition is always meaningful. In later discussion, when there is no confusion or we are not interested in the alphabet, we often omit the alphabet extension when we sequentially composite two processes which have different alphabets.

The conditional $P \triangleleft b \triangleright Q$ defines a choice on two alternatives P and Q in accordance with the initial value of b , where b is a program expression, containing only initial variables and always producing a Boolean result. If b is true, it then acts like P , otherwise it behaves like Q instead.

Definition 5.6 (conditional) *Provided $\alpha P = \alpha Q$, then*

$$\llbracket P \triangleleft b \triangleright Q \rrbracket \hat{=} b \wedge \llbracket P \rrbracket \vee \neg b \wedge \llbracket Q \rrbracket \quad \square$$

Please note that to make this definition meaningful, there is a constraint that the two conditional branches have the same alphabet. This is because the alphabet of a process should be fixed, and cannot depend on the value of b .

The sequential composition and conditional enjoy some algebraic properties.

Law 5.1 (; associative)

$$(P ; Q) ; R = P ; (Q ; R) \quad \square$$

Law 5.2 (; conditional left distributive)

$$(P \triangleleft b \triangleright Q) ; R = (P ; R) \triangleleft b \triangleright (Q ; R) \quad \square$$

The first healthiness condition (**M1**) says that a process can never undo its execution. In other words, for any valid observation, the final value of the trace tr' is always an expansion of the initial one tr .

The second healthiness condition (**M2**) indicates that the initial value of trace tr has no influence on a process. In other words, tr may be replaced by an arbitrary trace s and the events which the process itself involves keep the same as $(tr' - tr)$.

The third healthiness condition (**M3**) ensures that the sequential composition works as supposed. Let us consider the sequential composition of two processes $X;P$. Control can pass from X to P only when X terminates, indicated by the fact $wait'_X = wait_P = false$. If the predecessor X is in a waiting state, indicated by the fact $wait'_X = wait_P = true$, the process P will leave the state unchanged.

The fourth one (**M4**) states that if a process has not started, then we cannot predict its behaviour except that its final trace tr' is an expansion of its initial one tr .

The fifth healthiness condition (**M5**) shows the fact that divergence is something that is never wanted. This is characterised by the monotonicity of ok' : if P is not divergent, indicated by $ok = true$, then we can say $P;J$ is not divergent as well, indicated by $ok' = true$; but if P is divergent, indicated by $ok = false$, we cannot insist on the divergence of $(P ; J)$, indicated by ok' can be either $true$ or $false$.

Refinement ordering between program variables is adopted in the above healthiness conditions. Identity I does not change any value of data and observation variables but allows process variables to be improved. It is the left unit of sequential composition of any predicate.

Law 5.3 (Left unit for predicate) *Any predicate $Pred$ satisfies*

$$I; Pred = Pred$$

Proof:

$$\begin{aligned}
(1) \quad & I; Pred \sqsupseteq Pred \\
& I; Pred && \{\text{def of } I \text{ and } ;\} \\
& = \exists v_0, \mathbf{obs}_0 \bullet v_0 \sqsupseteq v \wedge \mathbf{obs}_0 = \mathbf{obs} \wedge Pred(\mathbf{obs}_0, v_0, \mathbf{obs}', v') && \\
& && \{\text{one-point rule}\} \\
& = \exists v_0 \bullet v_0 \sqsupseteq v \wedge Pred(v_0, v') && \{\text{initial state monotonicity, } v_0 \sqsupseteq v\} \\
& \sqsupseteq (\exists v_0 \bullet v_0 \sqsupseteq v) \wedge Pred(v, v') && \{\text{let } v_0 = v\} \\
& = true \wedge Pred \\
& = Pred \\
(2) \quad & I; Pred \sqsubseteq Pred \\
& I; Pred && \{\text{def of } I \text{ and } ;\} \\
& = \exists v_0 \bullet v_0 \sqsupseteq v \wedge Pred(v_0, v') && \{(v_0 \sqsupseteq v) \sqsubseteq (v_0 = v)\} \\
& \sqsubseteq \exists v_0 \bullet v_0 = v \wedge Pred(v_0, v') && \{\text{one-point rule}\}
\end{aligned}$$

$$= \text{Pred} \quad \square$$

Law 5.4 (Left zero for J)

$$(tr \leq tr' ; J) = tr \leq tr'$$

Proof:

$$\begin{aligned}
& \text{LHS} && \{\text{def of } J, \text{ let } x \text{ contains } \text{ref}, \text{wait}, v\} \\
& = tr \leq tr' ; ok \Rightarrow ok' \wedge tr' = tr \wedge x' = x && \{\text{def of } ;\} \\
& = \exists tr_0, ok_0, x_0 \bullet tr \leq tr_0 \wedge ok_0 \Rightarrow ok' \wedge tr' = tr_0 \wedge x' = x_0 && \{\text{predicate calculus}\} \\
& = (\exists tr_0 \bullet tr \leq tr_0 \wedge tr' = tr_0) \wedge (\exists ok_0 \bullet ok_0 \Rightarrow ok') \wedge (\exists x_0 \bullet x' = x_0) && \\
& && \{\text{one-point rule, let } ok_0 = ok', x_0 = x'\} \\
& = (tr \leq tr') \wedge \text{true} \wedge \text{true} \\
& = tr \leq tr' && \square
\end{aligned}$$

Healthiness conditions satisfies some algebraic properties.

Theorem 5.1 (Property of M)

- (1) If P is **M14**, then P satisfies
 $P = (II ; P)$ and $(tr \leq tr' ; P) = tr \leq tr'$
- (2) If P satisfies $P = (II ; P)$ and $(tr \leq tr' ; P) = tr \leq tr'$,
then P is **M4**.

Proof:

$$\begin{aligned}
& (1.1) \\
& tr \leq tr' ; P && \{\text{assumption: } P \text{ is } \mathbf{M14}\} \\
& = tr \leq tr' ; (P \wedge tr \leq tr') \triangleleft ok \triangleright tr \leq tr' && \{\text{def of } ;\} \\
& = \exists ok_0, tr_0, \dots \bullet (tr \leq tr_0 \wedge ((P \wedge tr_0 \leq tr') \triangleleft ok_0 \triangleright tr_0 \leq tr')) && \\
& && \{\wedge\text{-distr-over-}\triangleleft\triangleright, \text{predicate calculus}\} \\
& = \exists ok_0, \dots \bullet (P \wedge tr \leq tr') \triangleleft ok_0 \triangleright (tr \leq tr') && \{ok_0 \text{ case analysis}\} \\
& = ((P \wedge tr \leq tr') \triangleleft ok_0 \triangleright (tr \leq tr'))[true/ok_0] \vee && \\
& \quad ((P \wedge tr \leq tr') \triangleleft ok_0 \triangleright (tr \leq tr'))[false/ok_0] && \{\text{substitution}\} \\
& = (P \wedge tr \leq tr') \vee (tr \leq tr') && \{\text{absorption}\} \\
& = tr \leq tr' \\
& (1.2) \\
& II ; P && \{\text{def of } II\} \\
& = (I \triangleleft ok \triangleright tr \leq tr') ; P && \{;-left-distr-over-\triangleleft\triangleright\} \\
& = (I ; P) \triangleleft ok \triangleright (tr \leq tr' ; P) && \{\text{Law 5.3, } (tr \leq tr' ; P) = tr \leq tr'\} \\
& = P \triangleleft ok \triangleright tr \leq tr' && \{\text{assumption: } P \text{ is } \mathbf{M4}\} \\
& = P \\
& (2)
\end{aligned}$$

$$\begin{aligned}
P \triangleleft ok \triangleright tr \leq tr' & \quad \{\text{Law 5.3, assumption: } (tr \leq tr'; P) = tr \leq tr'\} \\
= (I; P) \triangleleft ok \triangleright (tr \leq tr'; P) & \quad \{;\text{-left-distr-over-}\triangleleft\triangleright\} \\
= (I \triangleleft ok \triangleright tr \leq tr'); P & \quad \{\text{def of } II, \text{ assumption: } P = II; P\} \\
= P &
\end{aligned}$$

□

Definition 5.7 (MP process) *A predicate P is called an **MP** process or a healthy process if it satisfies $P = \mathbf{M12345}(P)$.* □

In a design calculus, for any operator to be useful, the combination of two healthy processes by this operator should be healthy as well. The fact is captured by the closure of **MP** processes under this operator, therefore we have the obligation to discuss and prove this issue when a new operator is defined.

Theorem 5.2 (MP process closure) *The set of **MP** processes form a complete lattice, which is a $\{\wedge, \vee, ;\}$ -closure.*

Proof: *Let α denote all alphabets except the mentioned one, we only show*
;-closure: $\mathbf{M12345}(P); \mathbf{M12345}(Q) = \mathbf{M12345}(P; Q)$

$$\begin{aligned}
\mathbf{M1}. P; Q & \quad \{P \text{ and } Q \text{ are } \mathbf{M1}\} \\
= P \wedge tr \leq tr'; Q \wedge tr \leq tr' & \quad \{\text{def of } ;\} \\
= \exists tr_0 \bullet (P; Q) \wedge tr \leq tr_0 \wedge tr_0 \leq tr' & \quad \{\text{predicate calculus}\} \\
= (P; Q) \wedge (tr \leq tr') & \\
= \mathbf{M1}(P; Q) &
\end{aligned}$$

$$\begin{aligned}
& \mathbf{M2}. P; Q && \{\text{def of } ; \} \\
& = \exists \alpha_0, tr_0 \bullet (P(\alpha, tr, \alpha_0, tr_0) \wedge Q(\alpha_0, tr_0, \alpha', tr')) && \{P \text{ and } Q \text{ are } \mathbf{M2}\} \\
& = \exists \alpha_0, tr_0 \bullet \left(\begin{array}{l} \prod_{s_1} P(\alpha, s_1, \alpha_0, s_1 \wedge (tr_0 - tr)) \wedge \\ Q(\alpha_0, s_1 \wedge (tr_0 - tr), \alpha', s_1 \wedge (tr_0 - tr) \wedge (tr' - tr_0)) \end{array} \right) \\
& && \{\text{def of } ; \} \\
& = \exists tr_0 \bullet \prod_{s_1} (P; Q)(s_1, s_1 \wedge (tr_0 - tr) \wedge (tr' - tr_0)) && \{\text{sequence calculation}\} \\
& = \prod_{s_1} (P; Q)(s_1, s_1 \wedge (tr' - tr)) \\
& = \mathbf{M2}(P; Q) \\
& \mathbf{M3}. P; Q && \{P \text{ is } \mathbf{M3}\} \\
& = (II \triangleleft wait \triangleright P); Q && \{;-left-distr-over-\triangleleft \triangleright\} \\
& = (II; Q) \triangleleft wait \triangleright (P; Q) && \{Q \text{ is } \mathbf{M14}, \text{ Theorem 5.1}\} \\
& = Q \triangleleft wait \triangleright (P; Q) && \{Q \text{ is } \mathbf{M3}\} \\
& = (II \triangleleft wait \triangleright Q) \triangleleft wait \triangleright (P; Q) && \{\text{def of conditional}\} \\
& = II \triangleleft wait \triangleright (P; Q) \\
& = \mathbf{M3}(P; Q) \\
& \mathbf{M4}. P; Q && \{P \text{ is } \mathbf{M4}\} \\
& = (P \triangleleft ok \triangleright tr \leq tr'); Q && \{;-left-distr-over-\triangleleft \triangleright\} \\
& = (P; Q) \triangleleft ok \triangleright (tr \leq tr'; Q) && \{Q \text{ is } \mathbf{M14}, \text{ Theorem 5.1}\} \\
& = (P; Q) \triangleleft ok \triangleright tr \leq tr' \\
& = \mathbf{M4}(P; Q) \\
& \mathbf{M5}. P; Q && \{Q \text{ is } \mathbf{M5}\} \\
& = P; (Q; J) && \{;-assoc\} \\
& = (P; Q); J \\
& = \mathbf{M5}(P; Q)
\end{aligned}$$

□

5.3 Higher-order assignment

Besides the refinement ordering between program variables, another difference with first-order programming in our approach is in the semantics of higher-order assignment and communication.

First-order assignment $t := e$ has no interaction with the environment. It always terminates and never diverges. On termination, it equates the final value of t with value e , but does not change the other variables.

Definition 5.8 (First-order assignment)

$$\llbracket t := e \rrbracket \hat{=} \mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge t' = e \wedge v' = v)$$

where the function application of $\mathbf{M34}$ to the predicate is to ensure that assignment is healthy. The satisfaction of the other healthiness conditions can be derived from the definition (see Section 5.4). □

In higher-order programming, we need change the semantics of higher-order assignment to allow an implementation of replacing the assigned value by anyone that refines it.

Definition 5.9 (Higher-order constant assignment) *Let $\alpha h = \alpha P$, $h, h' \notin \alpha P$*

$$\begin{aligned} \llbracket h := \{\{P\}\} \rrbracket &\hat{=} \mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge h' \sqsupseteq P \wedge v' \sqsupseteq v) \\ \alpha(h := \{\{P\}\}) &= \{\mathbf{obs}, \mathbf{obs}', h, h', v, v'\} \end{aligned}$$

where v are all program variables except h . □

The reason is to guarantee that assignment is monotonic with respect to refinement in the assigned value. This can be seen in the following theorem.

Theorem 5.3 (Assignment monotonicity) *Suppose that h is a higher-order variable.*

$$(P \sqsubseteq Q) \Rightarrow (h := \{\{P\}\} \sqsubseteq h := \{\{Q\}\})$$

Proof:

$$\begin{aligned} (P \sqsubseteq Q) &\Rightarrow (h := \{\{P\}\} \sqsubseteq h := \{\{Q\}\}) && \{\text{Def. 5.1 and 5.9}\} \\ &\equiv (P \sqsubseteq Q) \Rightarrow \\ &\quad \left[\begin{array}{l} \mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge h' \sqsupseteq Q \wedge v' \sqsupseteq v) \\ \Rightarrow \mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge h' \sqsupseteq P \wedge v' \sqsupseteq v) \end{array} \right] \\ &\quad \{\mathbf{M3} \text{ and } \mathbf{M4}, \text{ one-point, propositional calculus}\} \\ &\equiv (P \sqsubseteq Q) \Rightarrow [(ok' \wedge \neg wait' \wedge h' \sqsupseteq Q \wedge v' \sqsupseteq v) \Rightarrow (h' \sqsupseteq P)] \\ &\quad \{\text{universal closure, case analysis}\} \\ &\equiv (P \sqsubseteq Q) \Rightarrow [(h' \sqsupseteq Q) \Rightarrow (h' \sqsupseteq P)] && \{\text{universal closure, } h' \notin \alpha P, h' \notin \alpha Q\} \\ &\equiv \forall h' \bullet (P \sqsubseteq Q) \Rightarrow (h' \sqsupseteq Q \Rightarrow h' \sqsupseteq P) && \{\text{propositional calculus}\} \\ &\equiv \forall h' \bullet (P \sqsubseteq Q \wedge h' \sqsupseteq Q) \Rightarrow h' \sqsupseteq P && \{\text{transitivity of refinement}\} \\ &\equiv \forall h' \bullet (P \sqsubseteq Q \wedge h' \sqsupseteq Q \wedge h' \sqsupseteq P) \Rightarrow (h' \sqsupseteq P) && \{\text{propositional calculus}\} \\ &\equiv \forall h' \bullet true && \{\text{predicate calculus}\} \\ &\equiv true \end{aligned}$$

□

Adoption of equation in the definition of assignment will lead to a contradiction that $(P \sqsubseteq Q)$ implies $(P = Q)$:

$$\begin{aligned}
& h := \llbracket P \rrbracket \sqsubseteq h := \llbracket Q \rrbracket && \{\text{refinement (definition 5.1)}\} \\
& = [h := \llbracket Q \rrbracket \Rightarrow h := \llbracket P \rrbracket] && \{\text{assumption (definition of assignment)}\} \\
& = [\mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge h' = \llbracket Q \rrbracket \wedge v' = v) \Rightarrow \\
& \quad \mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge h' = \llbracket P \rrbracket \wedge v' = v)] \\
& && \{\text{definition of } \mathbf{M3} \text{ and } \mathbf{M4}\} \\
& = [ok' \wedge \neg wait' \wedge tr' = tr \wedge h' = \llbracket Q \rrbracket \wedge v' = v \Rightarrow \\
& \quad ok' \wedge \neg wait' \wedge tr' = tr \wedge h' = \llbracket P \rrbracket \wedge v' = v] \\
& && \{\text{one-point rule, three times}\} \\
& = [ok' \wedge \neg wait' \Rightarrow ok' \wedge \neg wait' \wedge \llbracket Q \rrbracket = \llbracket P \rrbracket] && \{\text{propositional calculus}\} \\
& = [ok' \wedge \neg wait' \Rightarrow \llbracket Q \rrbracket = \llbracket P \rrbracket] && \{\text{universal closure, case analysis}\} \\
& = (P = Q)
\end{aligned}$$

For the same reason, we also adopt the inequation in the definitions of higher-order communication and *I*, *II* and *SKIP*. For first-order data, two values are comparable if and only if they are equal. Therefore higher-order assignment or communication and *SKIP* are consistent with their counterpart in first-order programming. We extend the language, but we are interested in a conservative extension in which the new semantics is not only suitable for the extension part, but also for the original part.

5.4 Denotational semantics of mobile processes

5.4.1 Primitive processes

We first discuss some simple processes, which serve as primitives and normally need to be used in combination to exhibit more complex behaviours.

SKIP The process *SKIP* refuses to engage in any communication event, but terminates immediately. It allows improvement of all program variables.

Definition 5.10 (SKIP) Let $\alpha SKIP = A = \{\mathbf{obs}, \mathbf{obs}', v, v'\}$

$$\llbracket SKIP \rrbracket \cong \mathbf{M123}(\mathbf{true}_{\{ref, ref'\}}; II)$$

where $\mathbf{true}_{\{ref, ref'\}}$ is a predicate whose alphabet only contains *ref* and *ref'* that have arbitrary values. \square

The definition indicates that for process *SKIP*, the initial and final values of *ref* are entirely irrelevant. We adopt different notation $\mathbf{true}_{\{ref, ref'\}}$ to represent it from the existential quantification over *ref* in the definition of *SKIP* appeared in [13] chapter 9. The reason is to avoid the inconsistency in existential quantification with that in the definitions of sequential composition and variable declaration. In the definition of sequential composition, the existential quantification hides the intermediate observation and removes the variables that record it from the alphabet. In a later section, we will also see that in the definition of variable declaration, the existential quantification over a variable

removes this variable from the alphabet. In the definition of *SKIP*, however, *ref* should be in the alphabet.

Process *SKIP* is an *MP* process, but we only apply **M123** in its definition as **M4** and **M5** healthiness can be implied or proved by expanding its definition.

$$\begin{aligned}
& \llbracket \text{SKIP} \rrbracket && \{\text{def of } II, \text{ alphabet extension}\} \\
& = \mathbf{M123} \left(\begin{array}{l} \text{true}_{\{ref, ref'\}} \wedge \\ (ok' = ok \wedge wait' = wait \wedge tr' = tr \wedge v' \sqsupseteq v); \\ (obs' = obs \wedge v' \sqsupseteq v) \triangleleft ok \triangleright tr \leq tr' \end{array} \right) && \{\text{def of } ;\} \\
& = \mathbf{M123} \left(\begin{array}{l} \exists obs_0, v_0 \bullet \\ \text{true}_{\{ref, ref_0\}} \wedge \\ (ok_0 = ok \wedge wait_0 = wait \wedge tr_0 = tr \wedge v_0 \sqsupseteq v) \wedge \\ (obs' = obs_0 \wedge v' \sqsupseteq v_0) \triangleleft ok_0 \triangleright tr_0 \leq tr' \end{array} \right) \\
& && \{\text{ref, ref' are arbitrary, predicate calculus}\} \\
& = \mathbf{M123} \left(\begin{array}{l} (ok' \wedge wait' = wait \wedge tr' = tr \wedge v' \sqsupseteq v) \\ \triangleleft ok \triangleright \\ \left(\begin{array}{l} tr \leq tr' \wedge (\exists ok_0 \bullet ok_0 = ok) \wedge \\ (\exists wait_0 \bullet wait_0 = wait) \wedge (\exists v_0 \bullet v_0 \sqsupseteq v) \end{array} \right) \end{array} \right) \\
& && \{\text{let } ok_0 = ok, wait_0 = wait, v_0 = v\} \\
& = \mathbf{M123}((ok' \wedge wait' = wait \wedge tr' = tr \wedge v' \sqsupseteq v) \triangleleft ok \triangleright tr \leq tr') \\
& && \{\text{def of } \mathbf{M4}\} \\
& = \mathbf{M1234}(ok' \wedge wait' = wait \wedge tr' = tr \wedge v' \sqsupseteq v) && \{\text{def of } \mathbf{M1}\} \\
& = \mathbf{M234}(ok' \wedge wait' = wait \wedge tr' = tr \wedge v' \sqsupseteq v) && \{\text{def of } \mathbf{M2}, \mathbf{M3}\} \\
& = \sqcap_s \mathbf{M4}(II \triangleleft wait \triangleright (ok' \wedge wait' = wait \wedge s = s \wedge (tr' - tr) \wedge v' \sqsupseteq v)) \\
& && \{\text{calculation}\} \\
& = \mathbf{M4}(II \triangleleft wait \triangleright (ok' \wedge \neg wait' \wedge tr' - tr = \langle \rangle \wedge v' \sqsupseteq v)) && \{\text{def of } \mathbf{M3}\} \\
& = \mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v)
\end{aligned}$$

It is clear that *SKIP* satisfies **M4**.

Theorem 5.4 (SKIP healthy) *SKIP is an MP process.*

Proof: We only need to prove *SKIP* is **M5**. For easier presentation, let

$$A = (ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v)$$

$$B = (tr' = tr \wedge ref' = ref \wedge wait' = wait \wedge v' \sqsupseteq v)$$

$$\begin{aligned}
& \text{SKIP}; J && \{\text{def of } \text{SKIP}, I \text{ and } \mathbf{M34}\} \\
& = (I \triangleleft wait \triangleright A) \triangleleft ok \triangleright tr \leq tr'; J && \{;-left-distr-over-\triangleleft\triangleright, \text{def of } I\} \\
& = \left(\begin{array}{l} ok' \wedge B; J \\ \triangleleft wait \triangleright \\ ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v; J \end{array} \right) \triangleleft ok \triangleright (tr \leq tr'; J) \\
& && \{\text{def of } ; \text{ and Law 5.4}\} \\
& = ((ok' \wedge B) \triangleleft wait \triangleright A) \triangleleft ok \triangleright tr \leq tr' && \{\text{def of conditional}\} \\
& = ((ok' = ok \wedge B) \triangleleft wait \triangleright A) \triangleleft ok \triangleright tr \leq tr' && \{\text{def of } \mathbf{M34} \text{ and } \text{SKIP}\}
\end{aligned}$$

$$= SKIP$$

□

SKIP satisfies the following law.

Law 5.5 (Zero for *II*) $II; SKIP = SKIP; II = SKIP$

Proof:

$$\begin{aligned}
& II; SKIP && \{\text{def of } II, \text{ ;-distr-left-over-}\langle \triangleright \rangle\} \\
& = (I; SKIP) \langle \triangleright ok \triangleright (tr \leq tr'; SKIP) \{\text{Law 5.3, } SKIP \text{ is } \mathbf{M14}, \text{ Theorem 5.1}\} \\
& = SKIP \langle \triangleright ok \triangleright tr \leq tr' && \{SKIP \text{ is } \mathbf{M4}\} \\
& = SKIP \\
& SKIP; II && \{\text{def of } SKIP, \text{ ;-distr-left-over-}\langle \triangleright \rangle\} \\
& = (\mathbf{M3}(ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v); II) \langle \triangleright ok \triangleright (tr \leq tr'; II) \\
& \quad \{\text{def of } \mathbf{M3}, \text{ ;-distr-left-over-}\langle \triangleright \rangle, II \text{ is } \mathbf{M14}, \text{ Theorem 5.1}\} \\
& = ((II; II) \langle \triangleright wait \triangleright (ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v; II)) \\
& \quad \langle \triangleright ok \triangleright tr \leq tr' && \{\text{def of } ; \text{ and } II\} \\
& = (II \langle \triangleright wait \triangleright (ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v; I)) \langle \triangleright ok \triangleright tr \leq tr' \\
& \quad \{\text{def of } ;\} \\
& = (II \langle \triangleright wait \triangleright (ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v)) \langle \triangleright ok \triangleright tr \leq tr' \\
& \quad \{\text{def of } SKIP\} \\
& = SKIP
\end{aligned}$$

□

In standard theory of CSP, *SKIP* is the unit of sequential composition, but this feature cannot be proved from the semantics of *SKIP* and sequential composition. Therefore we specify it by two healthiness conditions **M6** and **M7**.

$$\mathbf{M6}(P) = SKIP; P$$

$$\mathbf{M7}(P) = P; SKIP$$

Theorem 5.5 (Property of **M6)** *MP process P satisfies **M6** iff*

$$\neg wait \Rightarrow (P = \mathbf{true}_{\{ref, ref'\}}; P)$$

Proof:

$$\begin{aligned}
& P = SKIP; P && \{\text{def of } SKIP, \text{ ; closure, } P \text{ is } \mathbf{M123}\} \\
& \Leftrightarrow \mathbf{M123}(P) = \mathbf{M123}(\mathbf{true}_{\{ref, ref'\}}; II; P) && \{P \text{ is } \mathbf{M14}, \text{ Theorem 5.1}\} \\
& \Leftrightarrow \mathbf{M123}(P) = \mathbf{M123}(\mathbf{true}_{\{ref, ref'\}}; P) && \{\text{def of } \mathbf{M3}\} \\
& \Leftrightarrow \mathbf{M12}(II \langle \triangleright wait \triangleright P) = \\
& \quad \mathbf{M12}(II \langle \triangleright wait \triangleright (\mathbf{true}_{\{ref, ref'\}}; P)) && \{\text{def of } \mathbf{M12}\} \\
& \Leftrightarrow wait \vee (P = \mathbf{true}_{\{ref, ref'\}}; P) && \{\text{propositional law}\}
\end{aligned}$$

$$\Leftrightarrow \neg \text{wait} \Rightarrow (P = \mathbf{true}_{\{\text{ref}, \text{ref}'\}}; P)$$

□

There is a similar theorem for **M7**-healthy process.

Theorem 5.6 (Property of M7) *MP process P satisfies M7 iff*

$$\neg \text{wait} \Rightarrow (P = P; \mathbf{true}_{\{\text{ref}, \text{ref}'\}})$$

Proof: *Similar to the proof of Theorem 5.5.*

□

Theorem 5.5 says that an **MP** process does not depend on the initial value of *ref* when *wait* is false. Of course, it must behave as required by **M3** when *wait* is *true*. Theorem 5.6 indicates that the value of *ref'* is irrelevant after the termination of the process.

STOP The deadlock process *STOP* is unable to communicate with its environment and always stays in a waiting state.

Definition 5.11 (STOP)

$$\llbracket \text{STOP} \rrbracket \hat{=} \mathbf{M4} \left(\begin{array}{l} \text{ok}' \wedge \text{wait}' \wedge \text{tr}' = \text{tr} \wedge \\ \text{wait} \Rightarrow (\text{ref}' = \text{ref} \wedge v' \sqsupseteq v) \end{array} \right)$$

□

If *STOP* is started in a divergent state (*ok* = *true*), then by **M4**, trace extension ($\text{tr} \leq \text{tr}'$) is all that we can guarantee about the resulting behaviour. Otherwise, if *STOP* is started in a stable state (*ok* = *false*), then *STOP* is stable as well (*ok'* = *true*), it does not terminate (*wait'* = *true*), and it does not change *tr*. If *STOP* is started in a state in which its sequential predecessor has not terminated (*wait* = *true*), then nothing changes including *ref*, but except the allowance of *v*'s improvement.

When *STOP* is started in a state in which its predecessor has terminated (*wait* = *false*), we cannot affirm any restriction on *v* and *ref*. This is because the values of *v* are not observable in a deadlock, and *ref* is arbitrary as *STOP* cannot perform any communication event.

Theorem 5.7 (STOP healthy) *STOP is an MP process.*

Proof: *We need to prove STOP is M1235. For easier presentation, let*

$$\begin{aligned} A &= (\text{ok}' \wedge \text{wait}' \wedge \text{tr}' = \text{tr} \wedge \text{wait} \Rightarrow (\text{ref}' = \text{ref} \wedge v' \sqsupseteq v)) \\ B &= (\text{tr}' = \text{tr} \wedge \text{wait}' = \text{wait} \wedge \text{ref}' = \text{ref} \wedge v' \sqsupseteq v) \end{aligned}$$

M1. Directly from the definition of *STOP*.

M2. Directly from the definition and $(s \leq s \wedge (\text{tr}' - \text{tr})) \equiv (\text{tr}' \leq \text{tr})$.

M3. $\text{II} \triangleleft \text{wait} \triangleright \text{STOP}$ {def of *II* and *STOP*}

$$\begin{aligned}
&= (I \triangleleft ok \triangleright tr \leq tr') \triangleleft wait \triangleright (A \triangleleft ok \triangleright tr \leq tr') \quad \{\text{def of conditional}\} \\
&= (I \triangleleft wait \triangleright A) \triangleleft ok \triangleright tr \leq tr' \quad \{\text{expand } I \text{ and } A\} \\
&= \left(\left(\begin{array}{l} (ok' \wedge wait' \wedge tr' = tr \wedge ref' = ref \wedge v' \sqsupseteq v) \\ \triangleleft wait \triangleright (ok' \wedge wait' \wedge tr' = tr) \\ \triangleleft ok \triangleright tr \leq tr' \end{array} \right) \right) \\
&\quad \{\text{propositional law}\} \\
&= (ok' \wedge wait' \wedge tr' = tr \wedge wait \Rightarrow (ref' = ref \wedge v' \sqsupseteq v)) \triangleleft ok \triangleright tr \leq tr' \\
&= STOP \\
\mathbf{M5. } STOP; J &\quad \{\text{definition of } STOP\} \\
&= (A \triangleleft ok \triangleright tr \leq tr'); J \quad \{;-distr-left-over-\triangleleft \triangleright\} \\
&= (A; J) \triangleleft ok \triangleright (tr \leq tr'; J) \quad \{\text{def of } ; \text{ and } J, \text{ Law 5.4}\} \\
&= \left(\begin{array}{l} \exists \mathbf{obs}_0, v_0 \bullet \\ ok_0 \wedge wait_0 \wedge tr_0 = tr \wedge wait \Rightarrow (ref_0 = ref \wedge v_0 \sqsupseteq v) \wedge \\ ok_0 \Rightarrow ok' \wedge tr' = tr_0 \wedge wait' = wait_0 \wedge ref' = ref_0 \wedge v' \sqsupseteq v_0 \end{array} \right) \\
&\quad \triangleleft ok \triangleright tr \leq tr' \quad \{\text{predicate calculus}\} \\
&= \left(\begin{array}{l} \exists ref_0, v_0 \bullet ok' \wedge wait' \wedge tr' = tr \wedge \\ ref' = ref_0 \wedge v' \sqsupseteq v_0 \wedge wait \Rightarrow (ref_0 = ref \wedge v_0 \sqsupseteq v) \end{array} \right) \\
&\quad \triangleleft ok \triangleright tr \leq tr' \quad \{\text{propositional laws, predicate calculus}\} \\
&= \left(\begin{array}{l} ok' \wedge wait' \wedge tr' = tr \wedge \\ \left(\begin{array}{l} \neg wait \wedge (\exists ref_0 \bullet ref' = ref_0) \wedge (\exists v_0 \bullet v' \sqsupseteq v_0) \\ \vee ref' = ref \wedge v' \sqsupseteq v \end{array} \right) \end{array} \right) \\
&\quad \triangleleft ok \triangleright tr \leq tr' \quad \{\text{let } ref_0 = ref', v_0 = v'\} \\
&= \left(\begin{array}{l} ok' \wedge wait' \wedge tr' = tr \wedge (\neg wait \vee ref' = ref \wedge v' \sqsupseteq v) \\ \triangleleft ok \triangleright tr \leq tr' \end{array} \right) \\
&\quad \{\text{propositional laws, def of } STOP\} \\
&= STOP
\end{aligned}$$

□

Following **M3**, as *STOP* is never terminated ($wait' = true$), its sequential successor is irrelevant and leaves the state unchanged. This feature is captured by a left zero law of sequential composition.

Law 5.6 (STOP left zero) $STOP; P = STOP$

Proof:

$$\begin{aligned}
&STOP; P \quad \{\text{def of } ; \text{ and } STOP, P \text{ is } \mathbf{M34}, \text{ ;-closure}\} \\
&= \mathbf{M4}(ok' \wedge wait' \wedge tr' = tr \wedge wait \Rightarrow (ref' = ref \wedge v' \sqsupseteq v); \mathbf{M3}(P)) \\
&\quad \{\text{def of } \mathbf{M3}\} \\
&= \mathbf{M4}(ok' \wedge wait' \wedge tr' = tr \wedge wait \Rightarrow (ref' = ref \wedge v' \sqsupseteq v); I) \quad \{\text{def of } ;\} \\
&= \mathbf{M4}(ok' \wedge wait' \wedge tr' = tr \wedge wait \Rightarrow (ref' = ref \wedge v' \sqsupseteq v)) \quad \{\text{def of } STOP\}
\end{aligned}$$

= *STOP*

□

CHAOS *CHAOS* is the worst *MP* process. Except that it satisfies healthiness conditions, we cannot say anything else about its behaviour.

Definition 5.12 (CHAOS)

$$\llbracket \text{CHAOS} \rrbracket \hat{=} \mathbf{M123}(\text{true})$$

□

We can expand the definition to get an alternative one.

$$\begin{aligned} \text{CHAOS} & \qquad \qquad \qquad \{\text{def of } \text{CHAOS} \text{ and } \mathbf{M3}\} \\ &= \mathbf{II} \triangleleft \text{wait} \triangleright \mathbf{M12}(\text{true}) \qquad \qquad \qquad \{\mathbf{M1}\} \\ &= \mathbf{II} \triangleleft \text{wait} \triangleright \mathbf{M2}(\text{true} \wedge tr \leq tr') \qquad \qquad \qquad \{\mathbf{M2}\} \\ &= \mathbf{II} \triangleleft \text{wait} \triangleright \prod_s (s \leq s \wedge (tr' - tr)) \\ &= \mathbf{II} \triangleleft \text{wait} \triangleright tr \leq tr' \end{aligned}$$

If *CHAOS* is started in a state in which its predecessor does not terminate (*wait* = *true*), then nothing changes. For example, *STOP*; *CHAOS* = *STOP*. Otherwise, it can only guarantee the trace is extended, but it cannot undo the interactions that the predecessor has already been involved. If its predecessor *MP* process *P* terminates but does not change *tr*, for example, *SKIP* and assignment, then the effect of *P*; *CHAOS* is the same as *CHAOS*.

Law 5.7 (CHAOS right zero)

- (1) $\mathbf{M3}(\neg \text{wait}' \wedge tr' = tr); \text{CHAOS} = \text{CHAOS}$
- (2) Let *Pred* be a predicate that does not contain *tr*, *tr'* and *wait'*, then
 $\mathbf{M3}(\text{Pred} \wedge \neg \text{wait}' \wedge tr' = tr); \text{CHAOS} = \text{CHAOS}$

Proof:

$$\begin{aligned} & (1) \text{ LHS} \qquad \qquad \qquad \{\text{def of } \mathbf{M3}, \text{;-distr-left-over-}\triangleleft\triangleright\} \\ &= (\mathbf{II}; \text{CHAOS}) \triangleleft \text{wait} \triangleright (\neg \text{wait}' \wedge tr' = tr; \text{CHAOS}) \\ & \qquad \qquad \qquad \{\text{def of } \mathbf{II}, \text{;-distr-left-over-}\triangleleft\triangleright, \text{def of } \text{CHAOS}\} \\ &= \left(\begin{array}{l} (\mathbf{I}; \text{CHAOS}) \triangleleft \text{ok} \triangleright (tr \leq tr'; \text{CHAOS}) \\ \triangleleft \text{wait} \triangleright \\ (\neg \text{wait}' \wedge tr' = tr; \mathbf{II} \triangleleft \text{wait} \triangleright tr \leq tr') \end{array} \right) \\ & \qquad \qquad \qquad \{\text{Law 5.3, Theorem 5.1, def of ;}\} \\ &= (\text{CHAOS} \triangleleft \text{ok} \triangleright tr \leq tr') \triangleleft \text{wait} \triangleright \\ & \qquad \qquad \qquad (\exists \text{wait}_0, tr_0, \dots \bullet \neg \text{wait}_0 \wedge tr_0 = tr \wedge \mathbf{II} \triangleleft \text{wait}_0 \triangleright tr_0 \leq tr') \\ & \qquad \qquad \qquad \{\text{CHAOS is } \mathbf{M4}, \text{wait}_0 \text{ case analysis, one-point rule}\} \\ &= \text{CHAOS} \triangleleft \text{wait} \triangleright tr \leq tr' \qquad \qquad \qquad \{\text{def of } \text{CHAOS}\} \\ &= (\mathbf{II} \triangleleft \text{wait} \triangleright tr \leq tr') \triangleleft \text{wait} \triangleright tr \leq tr' \qquad \qquad \qquad \{\text{def of conditional}\} \\ &= \mathbf{II} \triangleleft \text{wait} \triangleright tr \leq tr' \qquad \qquad \qquad \{\text{def of } \text{CHAOS}\} \\ &= \text{CHAOS} \end{aligned}$$

$$\begin{aligned}
& (2) \text{ Let } Pred = Pred(\mathbf{obs}, v, ok', ref', v') \\
& \text{LHS} \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{def of } \mathbf{M3}, \text{ ;-distr-left-over-} \triangleleft \triangleright \} \\
& = (II; \text{CHAOS}) \triangleleft \text{wait} \triangleright (Pred \wedge \neg \text{wait}' \wedge tr' = tr; \text{CHAOS}) \\
& \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{same as that in (1), def of } \text{CHAOS} \} \\
& = \text{CHAOS} \triangleleft \text{wait} \triangleright (Pred \wedge \neg \text{wait}' \wedge tr' = tr; II \triangleleft \text{wait} \triangleright tr \leq tr') \\
& \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{def of } \text{CHAOS} \text{ and conditional, def of ;, one-point rule} \} \\
& = II \triangleleft \text{wait} \triangleright (\exists ok_0, ref_0, v_0 \bullet Pred[ok_0, ref_0, v_0/ok', ref', ok'] \wedge tr \leq tr') \\
& \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{substitution} \} \\
& = II \triangleleft \text{wait} \triangleright ((\exists ok', ref', v' \bullet Pred) \wedge tr \leq tr') \qquad \qquad \{ \text{predicate calculus} \} \\
& = II \triangleleft \text{wait} \triangleright tr \leq tr' \qquad \qquad \qquad \qquad \qquad \{ \text{def of } \text{CHAOS} \} \\
& = \text{CHAOS}
\end{aligned}$$

□

Theorem 5.8 (CHAOS healthy) *CHAOS is an MP process.*

Proof: Let $A = (tr' = tr \wedge ref' = ref \wedge wait' = wait \wedge v' \sqsupseteq v)$

$$\begin{aligned}
& \mathbf{M4.} \text{CHAOS} \triangleleft ok \triangleright tr \leq tr' \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{def of } \text{CHAOS} \} \\
& = (II \triangleleft \text{wait} \triangleright tr \leq tr') \triangleleft ok \triangleright tr \leq tr' \qquad \qquad \{ \text{expand } II \text{ when } ok = true \} \\
& = (I \triangleleft \text{wait} \triangleright tr \leq tr') \triangleleft ok \triangleright tr \leq tr' \\
& = I \triangleleft (ok \wedge \text{wait}) \triangleright tr \leq tr' \\
& = (I \triangleleft ok \triangleright tr \leq tr') \triangleleft \text{wait} \triangleright tr \leq tr' \\
& = II \triangleleft \text{wait} \triangleright tr \leq tr' \\
& = \text{CHAOS} \\
& \mathbf{M5.} \text{CHAOS}; J \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{def of } \text{CHAOS} \} \\
& = ((I \triangleleft ok \triangleright tr \leq tr') \triangleleft \text{wait} \triangleright tr \leq tr'); J \qquad \qquad \{ \text{;-distr-left-over-} \triangleleft \triangleright \} \\
& = ((I; J) \triangleleft ok \triangleright (tr \leq tr'; J)) \triangleleft \text{wait} \triangleright (tr \leq tr'; J) \\
& \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{expand } I \text{ when } ok = true \} \\
& = ((ok' \wedge A; J) \triangleleft ok \triangleright (tr \leq tr'; J)) \triangleleft \text{wait} \triangleright (tr \leq tr'; J) \\
& \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \qquad \{ \text{def of ; and } J, \text{ Law 5.4 and Theorem 5.1} \} \\
& = ((ok' \wedge A) \triangleleft ok \triangleright tr \leq tr') \triangleleft \text{wait} \triangleright tr \leq tr' = II \triangleleft \text{wait} \triangleright tr \leq tr' \\
& = \text{CHAOS}
\end{aligned}$$

□

Variable Declaration To introduce a new variable p , we use the form of declaration ***vid** $p: T$* to permit the variable p of type T to be used in the portion of the program that follows it. Correspondingly, undeclaration ***end** p* terminates the region of permitted use of p . The portion of program P in which the variable p may be used is called its *scope*. In this case, p is called a *local variable* or *bound variable* of P .

The variables presented in the alphabet are *global variables* or *free variables*, and their values can be recorded at the initial state, the intermediate state, and on termination. Local variables represent the internal state, which is inaccessible from outside.

The variable declaration *vid* p behaves like *SKIP* except that after the declaration, the variable p becomes an invisible local variable, and is removed from the alphabet.

Definition 5.13 (Variable declaration/undeclaration) Let $p, p' \in A$,

$$\begin{aligned} \llbracket \mathbf{vid} \ p \rrbracket &\hat{=} \exists p \bullet \llbracket \mathbf{SKIP}_A \rrbracket \\ \llbracket \mathbf{end} \ p \rrbracket &\hat{=} \exists p' \bullet \llbracket \mathbf{SKIP}_A \rrbracket \\ \alpha(\mathbf{vid} \ p) &\hat{=} A \setminus \{p\} \\ \alpha(\mathbf{end} \ p) &\hat{=} A \setminus \{p'\} \end{aligned}$$

where $A \setminus \{p\}$ is a subset of A that contains all elements of A except p . \square

In the above definition, existential quantifier removes the related variable from alphabet A . This has already been shown in the definition of sequential composition, where the intermediate value of the variables are hidden by an existential quantifier over the intermediate variables. More generally, we have

$$\exists p \bullet Q_A = Q_{A \setminus \{p\}}$$

Alternately, variable declaration and undeclaration can be defined as

$$\begin{aligned} \llbracket \mathbf{vid} \ p \rrbracket &\hat{=} \llbracket \mathbf{SKIP}_{A \setminus \{p\}} \rrbracket \\ \llbracket \mathbf{end} \ p \rrbracket &\hat{=} \llbracket \mathbf{SKIP}_{A \setminus \{p'\}} \rrbracket \end{aligned}$$

As variable declaration and undeclaration are not homogenous, we need be care when use it in the conditional. They are not allowed to be used in the conditional separately except that the same variable declaration or undeclaration appears in both choice branches, or they appear as a pair in one branch. For instance, we do not allow $(\mathbf{vid} \ x; P) \triangleleft b \triangleright Q$ if $\mathbf{vid} \ x$ does not occur in Q and $\mathbf{end} \ x$ does not occur in P .

For more convenient presentation, we allow variables to be declared together

$$\begin{aligned} \mathbf{vid} \ p, q, \dots, r &\text{ rather than } \mathbf{vid} \ p; \mathbf{vid} \ q; \dots; \mathbf{vid} \ r \\ \mathbf{end} \ p, q, \dots, r &\text{ rather than } \mathbf{end} \ p; \mathbf{end} \ q; \dots; \mathbf{end} \ r \end{aligned}$$

and we enclose a variable by a block. We abbreviate $(\mathbf{proc} \ h; Q; \mathbf{end} \ h)$ by $(\mathbf{proc} \ h \bullet Q)$ to represent that the scope of h is valid in Q .

Theorem 5.9 (Variable declaration healthy) Variable declaration *vid* p and undeclaration *end* p are *MP* processes.

Proof: Direct from Definition 5.13 and Theorem 5.4. \square

Assignment After the declaration of a variable, it can be initialised by being the target of an assignment or a communication. The assignment terminates immediately, with the value of its target variable updated.

Higher-order constant assignment $h := \{\{P\}\}$ is defined as Definition 5.9. It is also suitable for first-order constant assignment. The alphabet of $(h := \{\{P\}\})$ does not include αP because P is a constant in the assignment, thus the αP is not relevant unless h is activated.

Mobile variable assignment makes a value, which is a data or a process, move from the source variable to the target one. On termination of the assignment $p :=_m q$, the source variable q is undefined, and has an arbitrary process value. However, it is still in the scope, and can be referred again if reinitialised.

Definition 5.14 (Mobile variable assignment) Let $\alpha p = \alpha q$, $p, p' \notin \alpha q$

$$\begin{aligned} \llbracket p :=_m q \rrbracket &\hat{=} \mathbf{M34} \left(\begin{array}{l} ok' \wedge \neg wait' \wedge (tr' = tr) \\ \wedge (p' \sqsupseteq q) \wedge (v' \sqsupseteq v) \end{array} \right) \\ \alpha(p :=_m q) &\hat{=} \{\mathbf{obs}, \mathbf{obs}', p, p', q, q', v, v'\} \end{aligned}$$

where v is the set of program variables except p and q . □

In the above definition, q' is in the alphabet but not mentioned in the predicate so that we can view its value is arbitrary. Alternatively, we can specify this fact by $q' \sqsupseteq \mathbf{CHAOS}$ which is always true.

Similar to the constant assignment, the clone variable assignment terminates immediately and updates the target variable in the light of the value of the source variable. Different from mobile assignment, the source variable is still defined on the termination of clone assignment.

Definition 5.15 (Clone variable assignment) Let $\alpha p = \alpha q$, $p, p' \notin \alpha q$

$$\begin{aligned} \llbracket p := q \rrbracket &\hat{=} \mathbf{M34} \left(\begin{array}{l} ok' \wedge \neg wait' \wedge (tr' = tr) \\ \wedge (p' \sqsupseteq q) \wedge (q' \sqsupseteq q) \wedge (v' \sqsupseteq v) \end{array} \right) \\ \alpha(p := q) &= \{\mathbf{obs}, \mathbf{obs}', p, p', q, q', v, v'\} \end{aligned}$$

where v is the set of program variables except p and q . □

Theorem 5.10 (Assignment healthy) Assignment are **MP** processes.

Proof: Similar to the proof of Theorem 5.4 □

We allow assignment to be combined with declaration. Usually we write **proc** $h := \{\{Q\}\}$ instead of **proc** $h; h := \{\{Q\}\}$.

Variable activation Once initialised, a process variable h may be activated by executing its process constant value; therefore, a process can be replaced by the activation of a variable which has been assigned by this process constant before.

Law 5.8 (Copy-rule-1) Let $h, h' \notin \alpha Q$, then

$$(h := \{\{Q\}\}; \langle h \rangle) = (h := \{\{Q\}\}; Q)$$

Proof:

$$\begin{aligned}
& RHS && \{\text{meaning of } \langle h \rangle \text{ and Def. 5.9}\} \\
& = h := \{\{Q\}; \sqcap \{R \mid R \sqsupseteq Q\} && \{\text{Def. 5.22}\} \\
& = h := \{\{Q\}; R_1 \vee \cdots \vee R_n \vee Q \\
& && \{R_i \sqsupseteq Q, \text{ Def. 5.1, propositional calculus}\} \\
& = LHS
\end{aligned}$$

where process R_i refines Q and n can be infinite. \square

Even though we adopt inequality in higher-order assignment, this rule is an equality rather than a refinement from the proof.

If a parameterised process variable h has the value of

$$\lambda x : \text{var}(T_1), y : \text{val}(T_2), z : \text{res}(T_2) \bullet P$$

then the effect of the activation $h(ne, ve, re)$ is calculated by

$$h(ne, ve, re) \hat{=} \left(\begin{array}{l} \mathbf{vid} \ x := ne, y := ve, z; \\ P; \\ ne := x, re := z; \\ \mathbf{end} \ x, y, z \end{array} \right)$$

It initially assigns the values of actual parameters ne and ve to the formal name and value parameters of P , and executes P . On the termination of P , the values of the name and result parameters are passed back to the actual parameters ne and re .

Communication Same as higher-order assignment, higher-order communication is monotonic in the communicated value. If one communicated process Q is better than another one P , then the communication system with Q should be better than that with P .

The communication $ch.E$ is stable while waiting for synchronisation of its two communicating parties ($ch!$ or $ch!!$ and $ch?$). As soon as both of them are ready, the communication takes place to perform any communication event $ch.M$, with its communicated value M better than E , and then terminates, allowing the program variables to improve.

Definition 5.16 (Higher order communication)

$$\begin{aligned}
& \llbracket ch.E \rrbracket \hat{=} \\
& \sqcap_M \left\{ \mathbf{M34} \left(\begin{array}{l} \text{wait}' \wedge ch \notin \text{ref}' \wedge \text{tr}' = \text{tr} \wedge \text{ok}' \wedge v' \sqsupseteq v \\ \vee \\ \neg \text{wait}' \wedge \text{tr}' = \text{tr} \wedge \langle ch.M \rangle \wedge \text{ok}' \wedge v' \sqsupseteq v \end{array} \right) \middle| M \sqsupseteq E \right\}
\end{aligned}$$

where \sqcap_M represents an non-deterministic choice over the process M , which will be defined in a later section, and $ch \notin \text{ref}'$ represents that all the communication events over ch are not in the refusal set. \square

This definition is also suitable for first-order communication *ch.e*. When the communicated value is first-order data *e*, the internal choice will result in *e* itself, as *M* can only equate *e* for first-order data.

Theorem 5.11 (Communication monotonicity) *Communication is monotonic in the communicated value.*

$$(P \sqsubseteq Q) \Rightarrow (ch.P \sqsubseteq ch.Q)$$

Proof: *Similar to the proof of Theorem 5.3.* □

By the input prefix $ch?p \rightarrow P$, variable *p* accepts a message from the channel and then the program behaves like *P*.

Definition 5.17 (Input prefix)

$$ch?p \rightarrow P \hat{=} \square_{E \in Mes} \bullet (ch.E; p := E; P)$$

where *Mes* is the set of all messages (data or processes) that can be transferred on the channel *ch*. External choice \square is defined in a later subsection. □

The clone output prefix $ch!q \rightarrow P$ represents a program which is willing to transfer the value of process variable *q* through channel *ch* and then behaves as *P*. Mobile output prefix $ch!!q \rightarrow P$ has the same meaning of $ch!q \rightarrow P$ except that output variable *q* is undefined after the transfer.

Definition 5.18 (Clone output prefix)

$$ch!q \rightarrow P \hat{=} ch.q; P$$

□

Definition 5.19 (Mobile output prefix)

$$ch!!q \rightarrow P \hat{=} \square_M \left\{ \begin{array}{l} \mathbf{M34} \left(\begin{array}{l} wait' \wedge ch \notin ref' \wedge tr' = tr \wedge ok' \wedge v' \sqsupseteq v \wedge q' \sqsupseteq q \\ \vee (\neg wait' \wedge tr' = tr \wedge \langle ch.M \rangle \wedge ok' \wedge v' \sqsupseteq v) \end{array} \right) \\ | M \sqsupseteq q \end{array} \right\} ; P$$

where *v* are all program variables except *q*. □

Theorem 5.12 (Communication healthy) *Communication, input prefix and output prefix are MP processes.*

Proof: *Similar to the proof of Theorem 5.4.* □

5.4.2 Parallel composition

The parallel composition $P \parallel Q$ executes P and Q concurrently such that the events in the alphabet of both parties require their simultaneous participation, whereas the remaining events of the system occur independently. Event synchronisation is the only way in which one parallel process can affect its partner. The system terminates after both P and Q have terminated successfully, and it becomes divergent after either one of its components does so. An event can be refused by a parallel composition if either parallel party can refuse it.

We only take the observation variables obs to be the shared write variables between two parallel processes. Same as the parallel constraint in *occam* [2], shared write program variables are not considered in our language — a variable that is written in one component of the parallel may not appear in any other component of the parallel. For data variables, their values can be updated as the input of a communication or the target of an assignment, or as the output of a mobile communication or the source of a mobile assignment. For process variables, all of them are write variables. This is because the assignment to a variable p allows the improvement of all process variables v , and other primitive processes allow the improvement of all process variables. Therefore the read variables of a process are only those first-order variables that are not the source of a mobile assignment/communication or not the target of any assignment/communication. There is another constraint for process variables. When a process variable is in activation, it cannot be activated in parallel, or assigned to/from another variable, or communicated as input or output.

Let $Var(P)$ denote all program variables employed by process P , and $RdVar(P)$ denote a subset of $Var(P)$ whose values are never modified by P . The parallel composition is defined as:

Definition 5.20 (Parallel composition) *Suppose that $Var(P) \cap Var(Q) = RdVar(P) \cap RdVar(Q)$, then*

$$\begin{aligned} \mathcal{A}(P \parallel Q) &\cong \mathcal{A}P \cup \mathcal{A}Q \\ \llbracket P \parallel Q \rrbracket &\cong \llbracket (P(\mathit{obs}, 1.\mathit{obs}')) \wedge (Q(\mathit{obs}, 2.\mathit{obs}')) \rrbracket; M(1.\mathit{obs}, 2.\mathit{obs}, \mathit{obs}') \\ M &\cong \left(\begin{array}{l} \mathit{ok}' = (1.\mathit{ok} \wedge 2.\mathit{ok}) \wedge \\ \mathit{wait}' = (1.\mathit{wait} \vee 2.\mathit{wait}) \wedge \\ \mathit{ref}' = (1.\mathit{ref} \cup 2.\mathit{ref}) \wedge \\ (\mathit{tr}' - \mathit{tr}) = (1.\mathit{tr} - \mathit{tr}) \parallel (2.\mathit{tr} - \mathit{tr}) \end{array} \right); \llbracket SKIP \rrbracket \end{aligned}$$

□

The trace merge function $s \parallel t$ is defined the same as that in [24], in which $\{a, b\} \notin E(s) \cap E(t)$, $\{c, d\} \in E(s) \cap E(t)$, $a \neq b$, $c \neq d$, where $E(s)$ is the set

of events in s .

$$\begin{aligned}
s \parallel t &\cong t \parallel s \\
\langle \rangle \parallel \langle \rangle &\cong \{\langle \rangle\} \\
\langle \rangle \parallel \langle c \rangle &\cong \{\} \\
\langle \rangle \parallel \langle a \rangle &\cong \{\langle a \rangle\} \\
\langle c \rangle \wedge x \parallel \langle c \rangle \wedge y &\cong \{\langle c \rangle \wedge u \mid u \in x \parallel y\} \\
\langle a \rangle \wedge x \parallel \langle c \rangle \wedge y &\cong \{\langle a \rangle \wedge u \mid u \in x \parallel \langle c \rangle \wedge y\} \\
\langle c \rangle \wedge x \parallel \langle d \rangle \wedge y &\cong \{\} \\
\langle a \rangle \wedge x \parallel \langle b \rangle \wedge y &\cong \{\langle a \rangle \wedge u \mid u \in x \parallel \langle b \rangle \wedge y\} \\
&\quad \cup \{\langle b \rangle \wedge u \mid u \in \langle a \rangle \wedge x \parallel y\}
\end{aligned}$$

In the definition of parallel composition, $Var(P) \cap Var(Q) = RdVar(P) \cap RdVar(Q)$ excludes the possibility of shared write program variables between two parallel components. M is a predicate that merges the final values of observable variables 1. **obs** and 2. **obs** produced by the two processes respectively; the sequential composition with *SKIP* deals with the situation when either parallel process is divergent or waiting.

Let us consider $(A;SKIP)$, where A only merges the final values of observable variables produced by the two parallel processes.

If either parallel party is divergent, indicated by $ok'_A = false$, then according to the definitions of sequential composition and *SKIP* (**M4** healthy), all that we can guarantee about the behaviour of *SKIP* is trace extension and we cannot predict the final value of any other observation variables. Therefore the parallelism may not keep the good behaviours of the other process which is not divergent. Moreover, as *SKIP* is **M5** healthy and it is the unit of sequential composition, sequentially composing *SKIP* makes the parallelism **M5** healthy as well.

If either parallel party is waiting, indicated by $wait'_A = true$, then *SKIP* behaves as required by **M3** and keep the behaviours unchanged, such that the parallel composition does not terminate as well. If both parallel processes terminate, then $wait'_A = false$. According to the definition of *SKIP*, this indicates that the $A;SKIP$ terminates and the final value of ref is irrelevant after termination, making the parallel composition **M7** healthy.

Theorem 5.13 (**||-closure**) *The set of healthy processes is closed under the parallel composition ||.* \square

5.4.3 Iteration

If b is a condition, the notation $b * P$ repeats the process P as long as b is *true* before each iteration. More formally, it can be defined as the recursion.

Definition 5.21 (**iteration**)

$$\begin{aligned}
\llbracket b * P \rrbracket &\cong \mu X \bullet (\llbracket P \rrbracket; X) \triangleleft b \triangleright \llbracket SKIP \rrbracket \\
\alpha(b * P) &\cong \alpha P
\end{aligned}$$

where $\mu X \bullet F(X)$ stands for the weakest fixed point [31] of the recursive equation $X = F(X)$. \square

5.4.4 Internal choice

The internal choice $P \sqcap Q$ stands for a program which is executed by performing either P or Q , but the choice between them is made arbitrarily and nondeterministically.

Definition 5.22 (Internal choice) *Provided $\alpha P = \alpha Q$, then*

$$\begin{aligned} \llbracket P \sqcap Q \rrbracket &\hat{=} \llbracket P \rrbracket \vee \llbracket Q \rrbracket \\ \alpha(P \sqcap Q) &\hat{=} \alpha P \end{aligned} \quad \square$$

Alphabet restriction is required that the alphabets of two processes should be the same, otherwise the internal choice has no meaning. The reason is the same as the alphabet restriction of conditional choice.

5.4.5 External choice

The external choice $P \sqcup Q$ behaves as either one of the two processes, where the choice is made by the environment when it performs one of the first interactions or the first terminations without event offered by P and Q . If the first interaction or termination is the one in which Q has performed, then the rest of the behaviours of $P \sqcup Q$ is described by Q . Similarly, P can be selected by occurrence of an interaction possible for P but not for Q . Finally, if the first event is possible for both P and Q , then the choice between them is nondeterministic.

Definition 5.23 (External choice) *Provided $\mathcal{A}P = \mathcal{A}Q$, then*

$$\llbracket P \sqcup Q \rrbracket \hat{=} \mathbf{M5}(\llbracket P \rrbracket \wedge \llbracket Q \rrbracket) \triangleleft \llbracket STOP \rrbracket \triangleright (\llbracket P \rrbracket \vee \llbracket Q \rrbracket) \quad \square$$

The definition also says that before P and Q communicate with the environment and the choice has not been made, which is indicated by $STOP = true$, the observation is agreed by both P and Q , and only internal interaction can take place, and an event can be refused by $P \sqcup Q$ just if it can be refused by both of them.

The purpose of applying healthiness condition **M5** is to deal with divergent behaviour. When $\neg STOP$ is selected, according to the definition of $STOP$ and conditional,³

$$\neg STOP = \left(\begin{array}{l} ok \wedge (\neg ok' \vee \neg wait' \vee tr' \neq tr \vee \neg (wait \Rightarrow ref' = ref \wedge v' \sqsupseteq v)) \\ \vee \\ \neg ok \wedge \neg tr \leq tr' \\ \vee \\ \neg tr \leq tr' \wedge \left(\begin{array}{l} \neg ok' \vee \neg wait' \vee tr' \neq tr \\ \vee \neg (wait \Rightarrow ref' = ref \wedge v' \sqsupseteq v) \end{array} \right) \end{array} \right)$$

³ $\neg(P \triangleleft b \triangleright Q) = (b \wedge \neg P) \vee (\neg b \wedge \neg Q) \vee (\neg P \wedge \neg Q)$

By applying **M5**, we cannot insist that $P \sqcap Q$ be divergent even when ok' is *false*.

The reason of the alphabet restriction is the same as that for conditional and internal choice.

Theorem 5.14 (\sqcap -healthy) *The set of healthy processes is closed under the external choice \sqcap .* \square

5.4.6 Hiding

Let X be a set of events that are regarded as internal events of a process P . We use $P \setminus X$ to denote the process in which the events of X occur silently without participation or even the knowledge of the environment of P . $P \setminus X$ reaches a stable state only when P is stable and cannot perform any event of X . It reaches divergent if P does and it terminates when P has terminated.

Definition 5.24 (Hiding)

$$\begin{aligned} \mathcal{A}(P \setminus X) &\hat{=} \mathcal{A}P - X \\ \llbracket P \setminus X \rrbracket &\hat{=} \exists tra, refa \bullet \left(\begin{array}{l} \llbracket P \rrbracket[tra/tr', refa/ref'] \wedge \\ tr' = tra \downarrow (\mathcal{A}P - X) \wedge \\ refa = ref' \cup X \end{array} \right); SKIP \end{aligned}$$

\square

Similar to that in the definition of parallel composition, the sequential composition with *SKIP* makes the definition of hiding healthy.

When we want to hide some communication events, instead of writing the communication events in X , we simply use a channel set CH to denote hiding all the communication on the channels in CH .

Theorem 5.15 (Hiding closure) *The set of healthy processes is closed under the hiding $\setminus X$.* \square

6 Algebraic Properties and Laws

Besides some algebraic laws for *SKIP*, *STOP* and *CHAOS* in the previous section, we will present more algebraic properties and laws for other **MP** processes in this section. The proofs of these laws are based on the UTP semantics of mobile processes, some of which are largely straightforward and are omitted.

6.1 Variable declaration and undeclaration

As we have seen in the previous chapter, higher-order variables play an important role in our work of mobile processes, because process mobility is exhibited in mobile variable assignment and communication. In this subsection, we will present and prove a series of laws of variable declaration and undeclaration.

In spite of the changed definition of identity and some other operators, these laws are very similar to their counterparts in conventional non-mobile first-order programming.

Law 6.1 (Sequential declaration commutative) *The sequential composition of two variable declarations or undeclarations are commutative.*

- (1) $(\mathbf{vid} \ p; \mathbf{vid} \ q) = (\mathbf{vid} \ q; \mathbf{vid} \ p) = (\mathbf{vid} \ p, q)$
- (2) $(\mathbf{end} \ p; \mathbf{end} \ q) = (\mathbf{end} \ q; \mathbf{end} \ p) = (\mathbf{end} \ p, q)$
- (3) $(\mathbf{vid} \ p; \mathbf{end} \ q) = (\mathbf{end} \ q; \mathbf{vid} \ p) \quad \{p \text{ and } q \text{ are distinct}\}$

□

Law 6.2 (Variable elimination/introduction) *If a declared program variable is never used or initialised, its declaration has no effect.*

$$(\mathbf{vid} \ p; Q_{+p}; \mathbf{end} \ p) = Q \quad \{p, p' \notin \alpha Q\}$$

Alphabet extension is needed in order to balance alphabets.

Proof:

$$\begin{aligned}
& LHS && \{\text{Definition 5.13}\} \\
& = \exists p \bullet \mathit{SKIP}_A; Q; \exists p' \bullet \mathit{SKIP}_A && \{\text{predicate calculus}\} \\
& = \exists p \bullet (\mathit{SKIP}_A; Q); \exists p' \bullet \mathit{SKIP}_A && \{\mathbf{M6}\} \\
& = \exists p \bullet Q; \exists p' \bullet \mathit{SKIP}_A && \{p \notin \alpha Q, \text{predicate calculus}\} \\
& = Q; \exists p' \bullet \mathit{SKIP}_A && \{\text{predicate calculus, } p' \notin \alpha Q\} \\
& = \exists p' \bullet (Q; \mathit{SKIP}_A) && \{\mathbf{M7}\} \\
& = \exists p' \bullet Q && \{p' \notin \alpha Q\} \\
& = RHS
\end{aligned}$$

□

Law 6.3 $(\mathbf{vid} \ p; \mathbf{end} \ p) = \mathit{SKIP}$ □

Similarly, if an initialised program variable is never used, its initialisation has no effect.

Law 6.4 $(\mathbf{proc} \ h; h := \{Q\}; \mathbf{end} \ h) = \mathit{SKIP}$ □

But if the program variable is initialised by a mobile assignment or a mobile communication, its initialisation changes the value of the source variable to arbitrary, therefore the whole program is refined by SKIP .

Law 6.5 $(\mathbf{vid} \ p; p :=_m q; \mathbf{end} \ p) \sqsubseteq \mathit{SKIP}$ □

By applying the above three laws, we can eliminate a non-useful variable in the derication process, or we can include a process in the scope of a new variable, assuming this variable is not free in the process.

The following law states that, upon declaration, a variable may take any value within its type, the choice being arbitrarily non-deterministic, and this variable remains unchanged until it is assigned or input to.

Law 6.6 (Initial value arbitrary) $(\mathbf{proc} \ h) = \mathbf{proc} \ h := \{\{CHAOS\}\}$

Proof:

$$\begin{aligned}
& RHS && \{\text{syntax}\} \\
& = \mathbf{proc} \ h; \ h := \{\{CHAOS\}\} && \{\text{Definition 5.13 and 5.9}\} \\
& = (\mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v))_{A \setminus \{h\}}; \\
& \quad (\mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge h' \sqsupseteq CHAOS \wedge v' \sqsupseteq v))_A \\
& \quad \quad \{h' \text{ in the alphabet but not mentioned in the first predicate}\} \\
& = (\mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v \wedge \mathbf{true}_{h'}))_{A \setminus h}; \\
& \quad (\mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge \mathbf{true}_{h'} \wedge v' \sqsupseteq v))_A && \{-\text{closure}\} \\
& = \left(\mathbf{M34} \left(\begin{array}{l} ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v \wedge \mathbf{true}_{h'}; \\ ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v \wedge \mathbf{true}_{h'} \end{array} \right) \right)_{A \setminus \{h\}} && \{\text{def of ;}\} \\
& = (\mathbf{M34}(ok' \wedge \neg wait' \wedge tr' = tr \wedge v' \sqsupseteq v \wedge \mathbf{true}_{h'}))_{A \setminus \{h\}} && \{\text{def of SKIP}\} \\
& = \mathbf{SKIP}_{A \setminus h} \\
& = LHS
\end{aligned}$$

□

As variable declaration $\mathbf{proc} \ h$ assigns an arbitrary value to this variable, any assignment to this variable can make it more deterministic.

Law 6.7 (Initial value refinement) $(\mathbf{proc} \ h) \sqsubseteq (\mathbf{proc} \ h := \{\{Q\}\})$ □

The next law states that the sequential composition of $\mathbf{end} \ p$ with $\mathbf{vid} \ p$ has no effect whenever it is followed by an update of p that does not depend on the previous value of p .

Law 6.8 (Vacuous declaration) *If the type of q and p are same, and p does not occur in Q and q , then*

$$\begin{aligned}
(1) \quad & (\mathbf{end} \ p; \ \mathbf{proc} \ p := \{\{Q\}\}) = (p := \{\{Q\}\}) \\
(2) \quad & (\mathbf{end} \ p; \ \mathbf{vid} \ p := q) = (p := q)
\end{aligned}$$

□

As a special case of the above law, the sequential composition of variable undeclaration with the same variable declaration changes the value of this variable to arbitrary.

Lemma 6.1 $(\mathbf{end} \ p; \ \mathbf{proc} \ p) = (p := \{\{CHAOS\}\})$ □

Obviously from the above lemma, the sequential composition of **end** p with **proc** p is refined by **SKIP**.

Law 6.9 (**end** p ; **vid** p) \sqsubseteq **SKIP** □

When the same variable is declared in two sequential programs, the two declarations may be replaced by a single one.

Law 6.10 (**proc** p ; P ; **end** p ; **proc** q ; Q ; **end** q) \sqsubseteq (**proc** p ; P ; Q ; **end** p)

Proof: *direct from Law 6.9 and M6.* □

The above law may reduce non-determinism. Suppose Q is $q :=_m p$. On the left hand side, the final value of q is arbitrary. On the right hand side, however, p may be assigned a certain value in P , thus the final value of q would be a refinement of p . The right hand side is at least as deterministic as the left one.

Law 6.11 (Variable scope conditional distributivity) *Variable scope is distributed over conditional, if no interference occurs with the condition.*

$$\begin{aligned} & (\mathbf{vid} \ p; P \triangleleft b \triangleright Q; \mathbf{end} \ p) \\ & = (\mathbf{vid} \ p; P; \mathbf{end} \ p) \triangleleft b \triangleright (\mathbf{vid} \ p; Q; \mathbf{end} \ p) \ \{p \text{ not occur in } b\} \end{aligned}$$

□

Law 6.12 (Variable scope extension/shrinkage) *The scope of a variable may be extended by moving variable declaration in front of a process that contains no free occurrences of it, or moving variable undeclaration after this process.*

$$\begin{aligned} (1) \quad & (P; \mathbf{vid} \ p) = (\mathbf{vid} \ p; P_{+p}) \\ (2) \quad & (\mathbf{end} \ p; P) = (P_{+p}; \mathbf{end} \ p) \quad \{p, p' \notin \alpha P\} \end{aligned}$$

Proof: *We only show the proof of (1). (2) can be proved in the same way.*

$$\begin{aligned} & LHS && \{\text{Definition 5.13}\} \\ & = P; \exists p \bullet \mathbf{SKIP}_A && \{p \notin \alpha P\} \\ & = \exists p \bullet (P; \mathbf{SKIP}_A) && \{\mathbf{M67}\} \\ & = \exists p \bullet (\mathbf{SKIP}_A; P) && \{p \notin \alpha P\} \\ & = (\exists p \bullet \mathbf{SKIP}_A); P && \{\text{Definition 5.13}\} \\ & = RHS \end{aligned}$$

□

If we look at the above laws from the other way around, we can see the scope of the variable p is shrunk.

A variable q can be replaced by a new one p , by introducing the new variable p , which is not declared in the block of q , and assigning q to p .

Law 6.13 (Local variable renaming) *If Q contains occurrences of q but no occurrence of p , then*

$$(Q; \mathbf{end} q) = (\mathbf{vid} p; p :=_m q; Q[p/q]; \mathbf{end} p, q)$$

Proof:

$$\begin{aligned} & \text{RHS} && \{\text{Definition 5.14}\} \\ & = \mathbf{vid} p; p := q; Q; \mathbf{end} p, q && \{\text{Law 6.12}\} \\ & = \mathbf{vid} p; p := q; \mathbf{end} p; Q; \mathbf{end} q && \{\text{Law 6.4}\} \\ & = \mathbf{SKIP}; Q; \mathbf{end} q && \{\mathbf{M6}\} \\ & = \text{LHS} \end{aligned}$$

□

If a declared process variable does not occur in one of the partners of its successive parallel composition, its declaration does not affect this process.

Law 6.14 (Variable scope parallel distributivity) *Suppose Q contains no occurrence of p , and P contains no occurrence of q , then*

$$\mathbf{vid} p, q; (P \parallel Q); \mathbf{end} p, q = (\mathbf{vid} p; P; \mathbf{end} p) \parallel (\mathbf{vid} q; Q; \mathbf{end} q)$$

□

In the procedure to derive a distributed system from a centralised one, we can apply this law to narrow the scope of a variable to its relevant process, therefore the variable declaration can be bounded together with this process when the centralised specification is split.

6.2 Assignment

The value of a process variable is viewed as a process constant, and is not subject to substitutions for the variables it contains.

Law 6.15 (Process constant)

$$(P(x)_{+h}; h := \llbracket Q(x) \rrbracket) = (h := \llbracket Q(x) \rrbracket; P(x)_{+h})$$

$$\{h, h' \notin \alpha P, h, h' \notin \alpha Q\}$$

where $P(x)$ and $Q(x)$ are processes in which x occurs as a variable. □

The assignment of a constant to a process variable at the end of its scope is vacuous. In the other words, if the value of a variable is updated but never used, this update is irrelevant.

Law 6.16 (Vacuous constant assignment)

$$(h := \{\{Q\}\}; \mathbf{end} h) = (\mathbf{end} h) \quad \{h, h' \notin \alpha Q\}$$

□

The mobile assignment $(h :=_m g; \mathbf{end} h)$ would not have been vacuous, since the effect on g (making its value undefined) would persist beyond the end of h 's scope. The corresponding law for mobile variable assignment is stronger in that it requires both variable scopes to end.

Law 6.17 (Vacuous mobile variable assignment)

$$(p :=_m q; \mathbf{end} p; \mathbf{end} q) = (\mathbf{end} p; \mathbf{end} q) \quad \square$$

The effect of the assignment of a variable to itself is the same as *SKIP*.

Law 6.18 (Vacuous identity assignment)

$$(p := p) = (p :=_m p) = \mathit{SKIP} \quad \square$$

When the mobile assignment terminates, on the right hand side, p 's value becomes arbitrary; however, on the left hand side p gets a value that refines the original value of right-hand-side p . Therefore, the whole conjunctive effect is that p refines its original value, which is the same as *SKIP*.

Any non-identity mobile assignment can be converted to clone assignment.

Law 6.19 (Mobile assignment to clone assignment)

$$(p :=_m q) = (p := q; \mathbf{end} q; \mathbf{vid} q) \quad \{p \text{ and } q \text{ are distinct}\}$$

□

In spite of the introduction of mobile assignment, any kind of assignment can be reduced to assignment normal form – total non-mobile assignment – in which all the variables of the program appear on the left hand side in some standard order, and their related values are shown on the right hand side,

$$h, g, \dots, r := \{\{P\}\}, \{\{Q\}\}, \dots, \{\{R\}\}$$

by transforming mobile assignment to non-mobile assignment and adding identity clone assignment.

Law 6.20 (Assignment normal form)

$$(h :=_m g) = (h, g, \dots, r := g, \{\{CHAOS\}\}, \dots, r) \quad \{g \text{ and } h \text{ are distinct process variables}\}$$

$$(p := q) = (p, q, \dots, r := q, q, \dots, r) \quad \{p \text{ and } q \text{ are distinct variables}\}$$

□

Successive assignments to the same variable can be reduced to one.

Law 6.21 (Assignment mergence) *If g and h are process variables, and Q and R are process constants, then*

- (1) $(h := \llbracket Q \rrbracket; h := F(h)) = (h := F(Q))$
- (2) $(h := g; h := F(h)) = (h := F(g))$
- (3) $(h := \llbracket Q \rrbracket; h := \llbracket R \rrbracket) = (h := \llbracket R \rrbracket)$ { R not refer to h }

□

Law 6.22 (Assignment sequence)

$$(q := p; p := f(q)) = (q := p; p := f(p)) \quad \square$$

CHAOS is a right zero of sequential composition of assignment.

Law 6.23 (Assignment right zero) *If P is an assignment defined by Definition 5.9, 5.14 or 5.15, then*

$$P; \text{CHAOS} = \text{CHAOS}$$

Proof: *Directly from the definition of assignment and Law 5.7.* □

6.3 Process variable activation

Any intent to activate an uninitialised process variable leads to chaos.

Law 6.24 (Uninitialised activation)

$$(\text{proc } h; \langle h \rangle) = \text{CHAOS}$$

Proof:

$$\begin{aligned}
& \text{LHS} && \{\text{Law 6.6}\} \\
& = \text{proc } h := \llbracket \text{CHAOS} \rrbracket; \langle h \rangle && \{\text{Definition 5.13 and 5.9}\} \\
& = \text{SKIP}_{A \setminus h}; h := \llbracket \text{CHAOS} \rrbracket; \sqcap_R \{R \mid R \sqsupseteq \text{CHAOS}\} && \{\text{set theory, Def. 5.22}\} \\
& = \text{SKIP}_{A \setminus h}; (h := \llbracket \text{CHAOS} \rrbracket; \text{CHAOS}) && \{\text{Law 6.23}\} \\
& = \text{SKIP}_{A \setminus h}; \text{CHAOS} && \{\mathbf{M6}\} \\
& = \text{RHS}
\end{aligned}$$

□

The mobility of processes is expressed in the following law.

Law 6.25 (Undefined activation) For distinct process variables g and h .

$$(g :=_m h ; \langle h \rangle) = \text{CHAOS} \quad \{\text{Law 6.25.A}\}$$

$$(ch!!h \rightarrow \langle h \rangle) = ch.h ; \text{CHAOS} \quad \{\text{Law 6.25.B}\}$$

where ch is a channel name. □

Law 6.25 captures the fact that a mobile process has moved after assignment or communication, since its value has been passed to a new location (g , or the other end of the channel ch), and none of its behaviours is available at its old location (the higher-order variable h). In Law 6.25.A, as there is no communication with the environment, the update of g can not be observed, therefore the whole effect is the same as CHAOS ; however, in Law 6.25.B, the execution of h still leads to CHAOS , but this does not undo the communication that has already happened.

The proofs of the above two laws can be easily derived from Law 6.19, the definition of output, Law 6.24 and Law 6.23.

6.4 Prefix

The mobile output can be replaced by clone output.

Law 6.26 (Mobile output to clone output)

$$(ch!!q \rightarrow P) = (ch!q \rightarrow \text{end } q; \text{vid } q; P)$$

Proof: Directly from the definitions. □

6.5 Iteration

If a boolean condition b holds, then the iteration $b * P$ will execute P and iterate. Otherwise, it terminates immediately. In first-order programming, there is a law

$$(t := e; b * P) = t := e; (P; b * P) \triangleleft b[e/t] \triangleright \text{SKIP}$$

in which the initial value of b can be evaluated by substituting the occurrences of t in b with its value e . However, a similar one in our theory may not hold

$$(h := \{E\}; b * P) \stackrel{?}{=} h := \{E\}; (P; b * P) \triangleleft b[E/h] \triangleright \text{SKIP}$$

This is because we cannot evaluate the initial value of b by substituting h with E , as the value of h is non-deterministic.

The following two laws of conventional imperative language still hold.

Law 6.27 $\text{true} * \text{SKIP} = \text{CHAOS}$ □

Law 6.28 $\text{false} * P = \text{SKIP}$ □

6.6 Other processes

The algebraic laws for some other processes are largely same as their counterpart in non-mobile conventional imperative programming. We briefly list these laws in this section, without detailed explanation.

Property 6.1 (Parallel composition)

- (1) $P \parallel Q = Q \parallel P$
- (2) $P \parallel (Q \parallel R) = (P \parallel Q) \parallel R$
- (3) $SKIP \parallel P = P$
- (4) $CHAOS \parallel P = CHAOS$
- (5) $(P \triangleleft b \triangleright Q) \parallel R = (P \parallel R) \triangleleft b \triangleright (Q \parallel R)$ □

Property 6.2 (External choice)

- (1) $P \square P = P$
- (2) $P \square Q = Q \square P$
- (3) $(P \square Q) \square R = P \square (Q \square R)$
- (4) $P \square CHAOS = CHAOS$
- (5) $P \square STOP = P$
- (6) $P \square SKIP \sqsubseteq SKIP$
- (7) $P \square (Q \sqcap R) = (P \square Q) \sqcap (P \square R)$
- (8) $P \sqcap (Q \square R) = (P \sqcap Q) \square (P \sqcap R)$
- (9) $P; (Q \square R) = (P; R) \square (P; R)$
- (10) $(P \square Q); R = (P; R) \square (Q; R)$
- (11) $P \triangleleft b \triangleright (Q \square R) = (P \triangleleft b \triangleright Q) \square (P \triangleleft b \triangleright R)$
- (12) $P \square (Q \triangleleft b \triangleright R) = (P \square Q) \triangleleft b \triangleright (P \square R)$ □

Property 6.3 (Internal choice)

- (1) $P \sqcap P = P$
- (2) $P \sqcap Q = Q \sqcap P$
- (3) $(P \sqcap Q) \sqcap R = P \sqcap (Q \sqcap R)$
- (4) $CHAOS \sqcap P = CHAOS$
- (5) $P; (Q \sqcap R) = (P; Q) \sqcap (P; R)$
- (6) $(P \sqcap Q); R = (P; R) \sqcap (Q; R)$
- (7) $P \triangleleft b \triangleright (Q \sqcap R) = (P \triangleleft b \triangleright Q) \sqcap (P \triangleleft b \triangleright R)$
- (8) $P \sqcap (Q \triangleleft b \triangleright R) = (P \sqcap Q) \triangleleft b \triangleright (P \sqcap R)$ □

Property 6.4 (Hiding)

- (1) $P \setminus \{\} = P$
- (2) $P \setminus X \setminus Y = P \setminus (X \cup Y)$
- (3) $(a \rightarrow P) \setminus X = \begin{cases} a \rightarrow (P \setminus X), & \text{if } a \notin X; \\ P \setminus X, & \text{if } a \in X \end{cases}$
- (4) $(P; Q) \setminus X = (P \setminus X); (Q \setminus X)$

- (5) $(P \parallel Q) \setminus X = (P \setminus X) \parallel Q,$ { $X \notin \mathcal{A}Q$ }
(6) $ch.E \setminus CH = SKIP,$ { $ch \in CH$ }
(7) $P \setminus CH = P$ { P not communicate through any channel in CH }

□

7 Development Method for Mobile Processes

Our development procedure involves two main steps. In the first step we group similar pieces of specification as the value of a parameterised process variable (see Section 7.1). In the second step, by converting assignment into communication, we make the variable mobile: consequently the activations of the variable can be completed in different hosts over a network (see Section 7.2). We present the derivation procedure through a set of laws for *MP* processes.

7.1 Abstracting process variable

The first step of the derivation is to replace a process by assigning this process constant to a new declared process variable and following by its activation.

Law 7.1 (Copy-rule-2)

$$Q = (\mathbf{proc} \ h := \llbracket Q \rrbracket \bullet \langle h \rangle) \quad \{h, h' \notin \alpha Q\}$$

Proof.

$$\begin{aligned} & LHS && \{\text{variable introduction}\} \\ & = \mathbf{proc} \ h; Q; \mathbf{end} \ h && \{\text{constant assignment vacuous}\} \\ & = \mathbf{proc} \ h; Q; h := \llbracket Q \rrbracket; \mathbf{end} \ h && \{\text{process-constant}\} \\ & = \mathbf{proc} \ h; h := \llbracket Q \rrbracket; Q; \mathbf{end} \ h && \{\text{copy-rule-1}\} \\ & = \mathbf{proc} \ h; h := \llbracket Q \rrbracket; \langle h \rangle; \mathbf{end} \ h && \{\text{syntax}\} \\ & = RHS \end{aligned}$$

□

The law for parameterised processes differs in that we activate them by providing actual parameters.

Law 7.2 (Parameterised copy-rule)

$$\begin{aligned} Q(i, j, k) = & \\ & \mathbf{proc} \ h := \llbracket \lambda x : \text{var}(T_1), y : \text{val}(T_2), z : \text{res}(T_3) \bullet Q(x, y, z) \rrbracket \bullet \\ & \quad h(i, j, k) && \{h, h' \notin \alpha Q\} \end{aligned}$$

where $Q(i, j, k)$ is a parameterised process with actual name, value and result parameters i, j and k of type T_1, T_2 and T_3 respectively. □

We introduce two notations to represent a series of sequential compositions. An indexed sequential composition is the sequential composition of a series of processes in order.

Definition 7.1 (Indexed sequential composition)

$$(\mathbin{\circlearrowleft} i : 1 \dots n \bullet P_i) \hat{=} \begin{cases} SKIP & n = 0 \\ (\mathbin{\circlearrowleft} i : 1 \dots n-1 \bullet P_i); P_n & n \geq 1 \end{cases} \quad \square$$

Sequential compositions may be iterated over a sequence s .

Definition 7.2 (Iterated sequential composition) $\forall s \in \text{seq } T$

$$(\mathbin{\circlearrowleft} i : s \bullet P(i)) \hat{=} \begin{cases} SKIP & s = \langle \rangle \\ P(\text{head}(s)); (\mathbin{\circlearrowleft} i : \text{tail}(s) \bullet P(i)) & s \neq \langle \rangle \end{cases}$$

where i is one of the elements in the sequence of parameters, and P is a parameterised process, $\text{head}(s)$ is the first element of s , $\text{tail}(s)$ is a subsequence of s after removing its first element. \square

For example, we denote the program $(t := t + 2; t := t + 7; t := t + 5)$ as

$$\mathbin{\circlearrowleft} i : \langle 2, 7, 5 \rangle \bullet \{\{\lambda j : \text{val}(\mathbb{N}) \bullet t := t + j\}\}(i)$$

For a series of similar pieces of program, we may be able to assign the parameterised process to a newly-introduced process variable, and activate it in series with proper arguments.

Law 7.3 (Iterated parameterised copy-rule) *Suppose Q is a parameterised process with a value parameter of type I , then, for any sequence s ,*

$$\begin{aligned} & (\mathbin{\circlearrowleft} i : s \bullet \{\{\lambda j : \text{val}(I) \bullet Q(j)\}\}(i)) \\ &= \left(\begin{array}{c} \mathbf{proc} \ h := \{\{\lambda j : \text{val}(I) \bullet Q(j)\}\} \bullet \\ (\mathbin{\circlearrowleft} i : s \bullet h(i)) \end{array} \right) \quad \{h, h' \notin \alpha Q\} \end{aligned} \quad \square$$

For instance, by using this law, we have the following derivation

$$\begin{aligned} & (t := t + 2; t := t + 7; t := t + 5) = \\ & \left(\begin{array}{c} \mathbf{proc} \ h := \{\{\lambda j : \text{val}(\mathbb{N}) \bullet t := t + j\}\} \bullet \\ (h(2); h(7); h(5)) \end{array} \right) \end{aligned}$$

As a special case of the above law, when $s = \langle 1, 2, \dots, n \rangle$,

$$(\mathbin{\circlearrowleft} i : 1 \dots n \bullet P(i)) = \left(\begin{array}{c} \mathbf{proc} \ h := \{\{\lambda j : \text{val}(\mathbb{N}) \bullet P(j)\}\} \bullet \\ (\mathbin{\circlearrowleft} i : 1 \dots n \bullet h(i)) \end{array} \right)$$

In the same way, we have similar laws for an iterated parameterised process which has name or result parameters.

7.2 Moving process variable

Even though we group similar pieces of specification as the value of a newly introduced parameterised process variable and activate it at necessary occurrences (Law 7.3), the whole specification is still centralised. In order to achieve a distributed system, we may consider putting many activations of this variable in different distributed components. To make sure that the variables activated in different components have the same process values or similar structures, we initialise the variable at one component but make it mobile, transmitted from one distributed component to another one. It is necessary to introduce communication in this step. Actually, the assignment and the communication are semantically equivalent.

Law 7.4 (Assignment-communication equivalence)

$$\begin{aligned} (p := q) &= ((ch?p \rightarrow SKIP) \parallel (ch!q \rightarrow SKIP)) \setminus \{ch\} \\ (p :=_m q) &= ((ch?p \rightarrow SKIP) \parallel (ch!!q \rightarrow SKIP)) \setminus \{ch\} \end{aligned} \quad \square$$

We borrow the concepts of pipes and chaining in CSP [12, 24]. Pipes are special processes which have only two channels, namely an input channel *left* and an output channel *right*. For example, a pipe that recursively accepts a number from *left*, and output its double to *right* can be represented by:

$$\mu X \bullet left?p \rightarrow right!(p + p) \rightarrow X$$

Chaining links two pipes together as a new pipe.

Definition 7.3 (Chaining)

$$P \gg Q \hat{=} (P[mid/right] \parallel Q[mid/left]) \setminus \{mid\} \quad \square$$

It is clear that chaining operator is associative.

Law 7.5 (Chaining associative)

$$P \gg (Q \gg R) = (P \gg Q) \gg R \quad \square$$

We may define the indexed chaining which connects a series of processes in order as a long pipe.

Definition 7.4 (Indexed-chaining)

$$\left(\gg i : 1 \dots n \bullet P_i \right) \hat{=} \begin{cases} P_1 & n = 1 \\ \left(\gg i : 1 \dots n-1 \bullet P_i \right) \gg P_n & n > 1 \end{cases} \quad \square$$

We introduce a new notation: double chaining links two pipes as a ring. All the communications between pipes are hidden from the environment.

Definition 7.5 (Double-chaining)

$$P \ll \gg Q \hat{=} (P[mid_1, mid_2/right, left] \parallel Q[mid_1, mid_2/left, right]) \setminus \{mid_1, mid_2\}$$

□

The double chaining operator is commutative.

Law 7.6 (Double-chaining commutative)

$$P \ll \gg Q = Q \ll \gg P$$

□

A ring of processes can be viewed as a long chain with the two chain ends connected. The order of processes in the ring is important, but the connecting point of the chain can be arbitrary. In other words, the chain can be started from any process and ended at one of its backwards adjacent process. This feature is captured by the following law.

Law 7.7 (Exchange)

$$\begin{aligned} & P_1 \ll \gg (\gg i : 2 \dots n \bullet P_i) \\ = & P_k \ll \gg ((\gg i : k + 1 \dots n \bullet P_i) \gg (\gg i : 1 \dots k - 1 \bullet P_i)) \quad 1 < k < n \\ = & P_n \ll \gg (\gg i : 1 \dots n - 1 \bullet P_i) \end{aligned}$$

Proof. We show the proof of $P \ll \gg (Q \gg R) = Q \ll \gg (R \gg P)$

$$\begin{aligned} & P \ll \gg (Q \gg R) && \{\text{def of } \ll \gg\} \\ = & (P[m_1, m_2/r, l] \parallel (Q \gg R)[m_1, m_2/l, r]) \setminus \{m_1, m_2\} && \{\text{def of } \gg\} \\ = & (P[m_1, m_2/r, l] \parallel (Q[m/r] \parallel R[m/l])[m_1, m_2/l, r]) \setminus \{m_1, m_2, m\} && \{\text{def of pipe}\} \\ = & (P[m_1, m_2/r, l] \parallel Q[m_1, m/l, r] \parallel R[m, m_2/l, r]) \setminus \{m_1, m_2, m\} && \{\parallel\text{-commutative}\} \\ = & (Q[m_1, m/l, r] \parallel (R[m, m_2/l, r] \parallel P[m_1, m_2/r, l])) \setminus \{m_1, m_2, m\} && \{\text{def of } \gg\} \\ = & (Q[m_1, m/l, r] \parallel (R \gg P)[m, m_1/l, r]) \setminus \{m_1, m\} && \{\text{def of } \ll \gg\} \\ = & Q \ll \gg (R \gg P) \end{aligned}$$

□

The update of a variable p by an expression of p can be implemented by double chaining two pipes, where the first pipe mobile outputs p , while the second pipe receives the value of p , assigns it to r and then outputs the value of the updated variable immediately to the first pipe.

Law 7.8 (Delegation with double-chaining)

$$(p := f(p)) = \left(\begin{array}{l} (right!!p \rightarrow left?p \rightarrow SKIP) \\ \ll \gg (vid\ r; left?r \rightarrow right!f(r) \rightarrow end\ r) \end{array} \right)$$

where $f(p)$ is an expression of p .

Proof.

$$\begin{aligned}
& RHS && \{\text{Definition 7.5}\} \\
& = ((mid_1!!p \rightarrow mid_2?p \rightarrow SKIP) \parallel (\mathbf{vid} \ r; mid_1?r \rightarrow mid_2!f(r) \rightarrow \mathbf{end} \ r)) \\
& \quad \setminus \{mid_1, mid_2\} && \{\text{Law 6.14}\} \\
& = \mathbf{vid} \ r; ((mid_1!!p \rightarrow mid_2?p \rightarrow SKIP) \parallel (mid_1?r \rightarrow mid_2!f(r) \rightarrow SKIP)) \\
& \quad \setminus \{mid_1, mid_2\}; \mathbf{end} \ r && \{\text{Law 7.4, twice}\} \\
& = \mathbf{vid} \ r; r := p; p := f(r); \mathbf{end} \ r && \{\text{Law 6.22}\} \\
& = \mathbf{vid} \ r; r := p; p := f(p); \mathbf{end} \ r && \{\text{Law 6.12}\} \\
& = \mathbf{vid} \ r; r := p; \mathbf{end} \ r; p := f(p) && \{\text{Law 6.4}\} \\
& = SKIP; p := f(p) && \{\mathbf{M6}\} \\
& = LHS
\end{aligned}$$

□

As the update is executed in the second pipe, the value of p in the first pipe is irrelevant after its output and before its coming back. Therefore, we adopt mobile output for p . In the second pipe, $f(r)$ is not a variable but a value based on variable r , so that we use normal output for $f(r)$.

Similarly, the serial update of a variable can also be implemented by a ring of pipes, in which different updates are executed in different pipes.

Law 7.9 (Serial delegation with chaining)

$$(w := g(f(w))) = \left(\begin{array}{c} right!!w \rightarrow left?w \rightarrow SKIP \\ \langle\langle \rangle\rangle \left(\begin{array}{c} \mathbf{vid} \ p; left?p \rightarrow right!f(p) \rightarrow \mathbf{end} \ p \\ \gg \\ \mathbf{vid} \ q; left?q \rightarrow right!g(q) \rightarrow \mathbf{end} \ q \end{array} \right) \end{array} \right)$$

Proof. Similar to the proof of Law 7.8. □

In a more general rule, a series of updating p through different processes $F_i(p, p')$ can be replaced by a loop pipelining, in which the series of update task are allocated in different pipes.

Law 7.10 (Loop pipelining)

$$\left(\begin{array}{c} \textcircled{g} \ i : 1 \dots n \bullet F_i(p, p'); w := p \\ (right!!p \rightarrow left?p \rightarrow w := p) \\ \langle\langle \rangle\rangle \\ \gg \ i : 1 \dots n \bullet (\mathbf{vid} \ r; left?r \rightarrow F_i(r, r'); right!!r \rightarrow \mathbf{end} \ r) \end{array} \right)$$

Proof. Induction over n . □

In the right hand side of the above law, the value of p travels from the first pipe to the series of pipes. Its final value, which is stored in w , is retrieved after p 's travelling back from the series of pipes.

When updating is performed by a series of activations of the same process variable, we can move this process variable around the loop pipelining and distribute the activations in different pipes.

Lemma 7.1 (Loop pipelining) *Suppose that h is a parameterised process variable with a value parameter i and a name parameter t , then*

$$\begin{aligned}
 & (\textcircled{g} \ i : 1 \dots n \bullet h(i, t); w := t) = \\
 & \left(\begin{array}{l}
 (\text{right}!!h!!t \rightarrow \text{left}?h?t \rightarrow w := t) \\
 \langle\langle \\
 \gg i : 1 \dots n \bullet \\
 (\text{proc } g; \text{vid } r; \text{left}?g?r \rightarrow g(i, r); \text{right}!!g!!r \rightarrow \text{end } g, r)
 \end{array} \right)
 \end{aligned}$$

□

In practice, t is the local state of mobile process variable h . When the mobile process variable moves, it takes not only its process value but also its local state; however, in our current work, we have not formalised the local state of a mobile process, therefore we simply send the local state together with the process variable in multiple data transfer [24], which also says that the two variables h and t are output at the same time.

8 Decentralising the Data Centre

Using the laws in Section 7, we are able to show the derivation of the distributed system from the centralised one in Section 3. As the behaviour of the hosts to send out the information is not our main concern, we simply ignore this part in our specification.

In the centralised system, initially, the variable t is initialised to 0. The data centre then repeats the task of communicating with each host, obtaining the information and updating t until all the hosts have been processed. Finally the data centre outputs the data through channel *result*. This specification can be written as

$$\begin{aligned}
 & \text{var } t := 0 \bullet \\
 & (\textcircled{g} \ i : 1 \dots n \bullet c.i?v \rightarrow t := t + v); \text{result}!t \rightarrow \text{SKIP}
 \end{aligned}$$

where $c.i$ is the channel connecting the data centre and the i th host. We notice that similar pieces of specification involving input and update occurs iteratively, therefore we can assign a parameterised process to a process variable, and then activate it repeatedly with proper arguments. In the parameterised process, the initial value of t needs to be known at the beginning of every communication with the host, and certainly we need to store the result of t after its update, therefore we select t as a name parameter. By applying the

iterated parameterised copy rule (Law 7.2), we reach the following development:

$$\begin{aligned}
&= \{\text{iterated-parameterised-copy-rule}\} \\
&\mathbf{var} \ t := 0 \bullet \\
&\quad \left(\begin{array}{l} \mathbf{proc} \ p := \{\{\lambda j : \mathit{val}(1 \dots n); u : \mathit{var}(\mathbb{N}) \bullet c.j?v \rightarrow u := u + v\}\} \bullet \\ \quad \quad \quad (\text{\textcircled{§}} \ i : 1 \dots n \bullet p(i, t)) \end{array} \right); \\
&\quad \mathit{result}!t \rightarrow \mathit{SKIP}
\end{aligned}$$

The channel index j is only of relevance at the beginning of the variable activation, therefore it is a value parameter.

As the output $\mathit{result}!t$ does not contain variable p , it can be moved inside the scope of p . By an application of Law 6.12, we calculate the following:

$$\begin{aligned}
&= \{\text{variable-}p\text{-scope-extension}\} \\
&\mathbf{var} \ t := 0 \bullet \\
&\quad \left(\begin{array}{l} \mathbf{proc} \ p := \{\{\lambda j : \mathit{val}(1 \dots n); u : \mathit{var}(\mathbb{N}) \bullet c.j?v \rightarrow u := u + v\}\} \bullet \\ \quad \quad \quad \text{\textcircled{§}} \ i : 1 \dots n \bullet p(i, t); \mathit{result}!t \rightarrow \mathit{SKIP} \end{array} \right)
\end{aligned}$$

The task of updating t by activating p can be completed using a series of pipes. The whole specification can be replaced by double chaining this series of pipes with another pipe, in which the initial value of t is sent out and the final value of t is retrieved. As the intermediate values of t and process variable p are of no concern, we use mobile output for t and p . When the activations have been completed in every host, the process variable is of no use to us, therefore we discard it in the last-visited host n and only the variable w that stores the data is output to the first pipe. By applying the loop-pipelining law (Lemma 7.1), we get the following specification.

$$\begin{aligned}
&= \{\text{loop-pipelining}\} \\
&\mathbf{var} \ t := 0 \bullet \\
&\quad \left(\begin{array}{l} \mathbf{proc} \ p := \{\{\lambda j : \mathit{val}(1 \dots n); u : \mathit{var}(\mathbb{N}) \bullet c.j?v \rightarrow u := u + v\}\} \bullet \\ \quad \quad \quad \left(\begin{array}{l} \mathit{right}!!p!!t \rightarrow \mathit{left}?t \rightarrow \mathit{result}!t \rightarrow \mathit{SKIP} \\ \quad \quad \quad \langle\langle \rangle\rangle \\ \quad \quad \quad \gg \ i : 1 \dots (n-1) \bullet \\ \quad \quad \quad \left(\begin{array}{l} \mathbf{proc} \ q; \mathbf{var} \ w; \mathit{left}?q?w \rightarrow q(i, w); \\ \quad \quad \quad \mathit{right}!!q!!w \rightarrow \mathbf{end} \ q, w \end{array} \right) \\ \quad \quad \quad \gg \left(\begin{array}{l} \mathbf{proc} \ q; \mathbf{var} \ w; \mathit{left}?q?w \rightarrow q(n, w); \\ \quad \quad \quad \mathit{right}!!w \rightarrow \mathbf{end} \ q, w \end{array} \right) \end{array} \right)
\end{array} \right)
\end{aligned}$$

This specification is still centralised, as the scopes of p and t are valid for all the pipes. We notice, however, these two variables do not occur in the pipes involved in updating, therefore we can take these pipes out of the scope of p and t . Applying the variable scope shrinkage (Law 6.12), we reach a distributed

system.

$$\begin{aligned}
&= \{\text{variable-}t\text{-and-}p\text{-scope-shrinkage}\} \\
&\left(\begin{array}{l} \mathbf{var} \ t := 0 \bullet \\ \mathbf{proc} \ p := \{\{\lambda j : \text{val}(1 \dots n); u : \text{var}(\mathbb{N}) \bullet c.j?v \rightarrow u := u + v\}\} \bullet \\ \text{right}!!p!!t \rightarrow \text{left}?t \rightarrow \text{result}!t \rightarrow \text{SKIP} \end{array} \right) \\
&\langle\langle \rangle\rangle \\
&\left(\begin{array}{l} \gg i : 1 \dots (n-1) \bullet \\ \mathbf{proc} \ q; \mathbf{var} \ w; \text{left}?q?w \rightarrow q(i, w); \text{right}!!q!!w \rightarrow \mathbf{end} \ q, w \\ \gg (\mathbf{proc} \ q; \mathbf{var} \ w; \text{left}?q?w \rightarrow q(n, w); \text{right}!!w \rightarrow \mathbf{end} \ q, w) \end{array} \right)
\end{aligned}$$

In the distributed system, the data centre and the hosts are arranged as a ring. The first component of the double chaining is the data centre, in which the variables t and p are mobile sent out, along channel $right$, after initialisation. The hosts are specified as a series of pipes, in which the process variable and the data are received from channel $left$, and then output to channel $right$ after activation of the process variable.

9 Conclusions and Future Work

We have presented the UTP denotational semantics of mobile processes and a set of laws for the development of mobile distributed systems. The correctness of these laws can be guaranteed by the semantics, and we have proved most of the presented. Through a simple example that can be implemented in both a centralised and a distributed fashion, we have shown these laws are suitable for a step-wise development, starting with a centralised specification and ending up with a distributed implementation, while we cannot achieve this using the π -calculus.

Our current work on the semantics of mobile processes mainly focuses on their mobility, and our development method applies to an initial specification without any mobile process; however, we simply ignore the process type and rich data structure within a mobile process. In occam_M [1], a process type determines an interface, a mobile process can implement multiple process types, and the value of a process variable is an instance of a mobile process that implements the type of this variable. Therefore, the activation of a process variable is determined by its type, and the process from which it is initialised. We formalise these issues, and then study the refinement of a mobile process itself.

In occam_M [1], channels have mobility. Channel variables reference only one of the ends of a channel bundle and those ends are mobile, and can be passed through channels. Moving one of the channel-bundle ends around a network enables processes to exchange communication capabilities, making the communication highly flexible. We intend to formalise this channel-ends mobility in the UTP and study its refinement calculus.

In the example in this paper, the derivation from the centralised system to the distributed one centres around the introduction of a higher-order variable.

Actually, moving processes can decrease network cost by replacing remote communication with local communication. Furthermore, after a mobile assignment or a mobile communication, the source variable is undefined and its allocated memory space is released to the environment. Clearly, consuming less network cost and occupying less memory space can be a performance enhancement, and in this sense, we have not demonstrated a performance improvement in our mobile system. We intend to investigate techniques for reasoning about this.

Our main objective is to include the semantics of mobile processes and its associated refinement calculus in *Circus* [34], a unified language for describing state-based reactive systems. The semantics [35] of *Circus* is based on UTP and a development method for *Circus* based on refinement [25, 3, 4] has been proposed. This inclusion will enhance the *Circus* model and allow *Circus* specification to reason about mobility.

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